



## **Tutorial Notes**

### **Tutorial 1: Design of Continuous-Time Filters from 0.1 Hz to 2.0 GHz**

**Presented by:**

**Edgar Sánchez-Sinencio, Texas A&M University  
José Silva-Martínez, Texas A&M University**

**Sunday Morning, May 23, 08:30 - 11:30**



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# DESIGN OF CONTINUOUS-TIME FILTERS FROM 0.1 HZ TO 2.0 GHZ

by: Edgar Sánchez-Sinencio & José Silva-Martínez,

E. Sanchez-Sinencio

**Texas A&M University** at IEEE ISCAS'04

J. Silva-Martinez

## DESIGN OF CONTINUOUS-TIME FILTERS FROM 0.1 HZ TO 2.0 GHZ



- Introduction and Motivation
- Transconductance Amplifier Topologies
- Linearized OTA techniques
- Low Frequency Transconductance Amps
- LF Design Examples
- High Frequency Transconductance Amps
- HF Design Examples
- RF Filters
- Tuning
- Conclusions and Open Problems

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# Continuous-Time Filters from 0.1Hz to 2.0GHz

## Outline

- **Introduction and Motivation**
- **A family of Transconductance for different frequency ranges (applications).**
- **Common-mode feedforward and feedback strategies needed for differential output filters.**
- **Frequency- and Q-tuning techniques for OTA-C filters**

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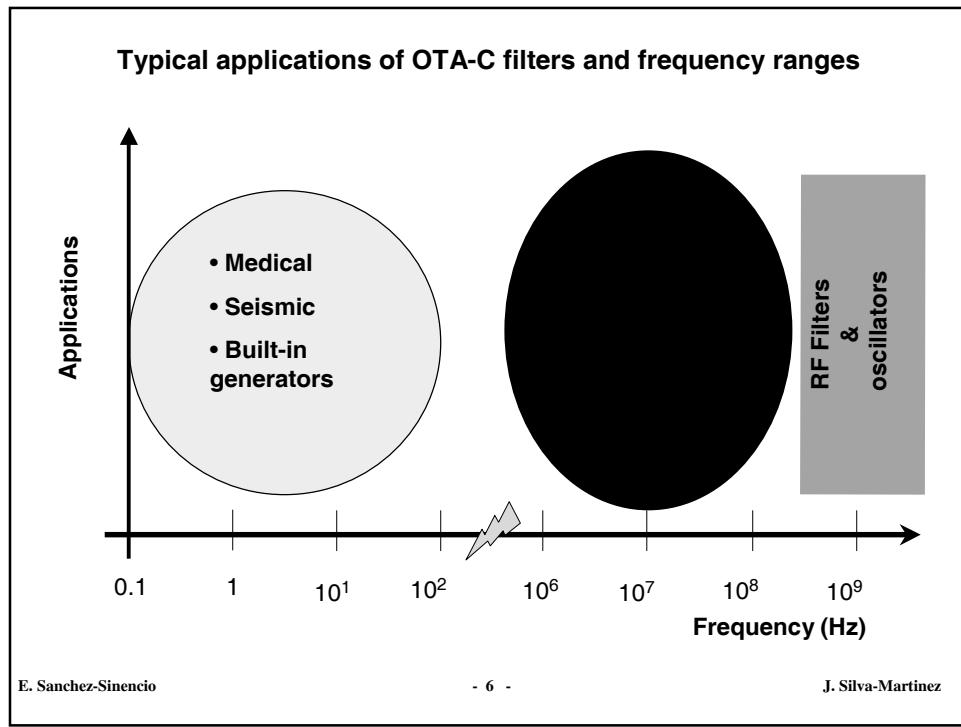
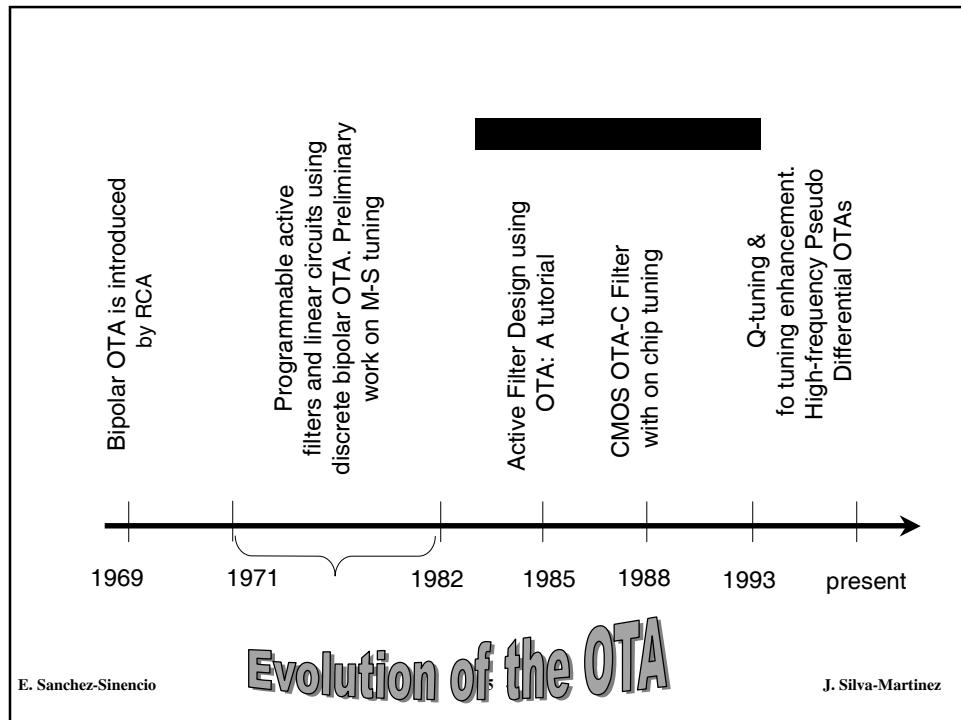
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## Continuous-Time Filters from 0.1Hz to 2.0 GHz

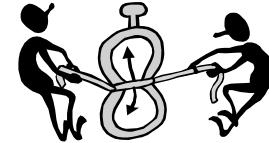
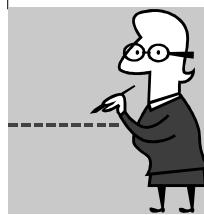
Edgar Sánchez-Sinencio  
Analog and Mixed-Signal Center, Texas A&M University  
E-mail: [sanchez@ee.tamu.edu](mailto:sanchez@ee.tamu.edu)

**Abstract.**-The bipolar transconductance amplifier (OTA) was commercially introduced in 1969 by RCA. Designers began using OTAs in the middle 80's, since then the CMOS-OTA has become a vital component in a number of electronic circuits, both in open loop and in closed loop applications. Here, we will focus on open loop applications. Continuous-time filters implemented with transconductance amplifiers and capacitors known as Gm-C or OTA-C are very popular for a host of applications. These applications involve frequency of operation from a few tens of a hertz up to several gigahertz. Several of those applications are in medical electronics and seismic area where the frequency range is between 0.1Hz up to 20Hz. Other applications in the audio range do not commonly use OTA-C filters because switched-capacitor techniques excel in this range. But for frequency range of a few MHz like in Intermediate Frequency (IF) filters in RF receivers OTA-C implementations are very attractive. For a few GHz range applications where the OTA becomes a simple differential pair there is number of researchers investigating LC-oscillators and filters. In this tutorial we discuss practical implementations of transconductance amplifiers oriented for wide range of applications for example in medical, IF filters, hard disk drive linear phase filters, LC-oscillators and RF filters. Furthermore the unavoidable tuning scheme to compensate the Gm/C deviations due to process technology variations is discussed. OTA single ended, fully differential and pseudo differential versions are introduced together with the common-

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## A few examples of continuous-time filters in a host of applications



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## Applications for continuous time filters



Read channel of disk drives

--

for phase equalization and  
smoothing the wave form

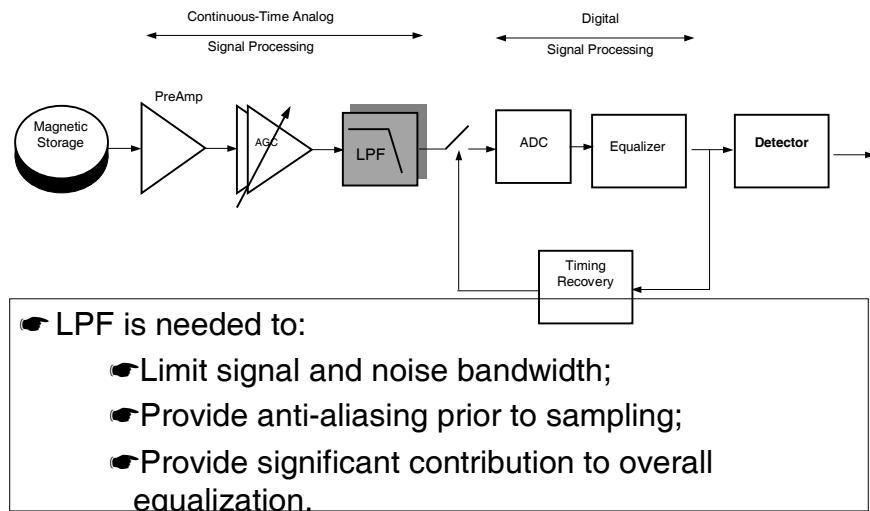
Top view of a 36 GB, 10,000 RPM,  
IBM SCSI server hard disk, with its  
top cover removed.

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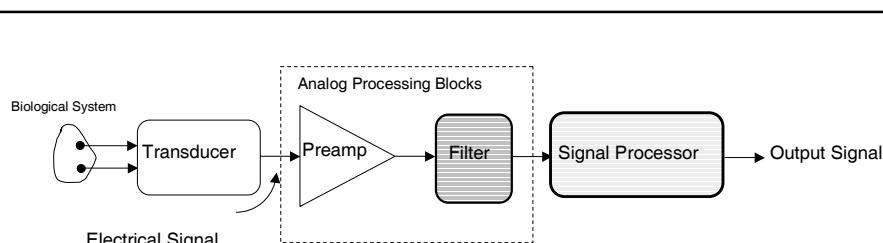
## Hard Disk Driver Read Channel



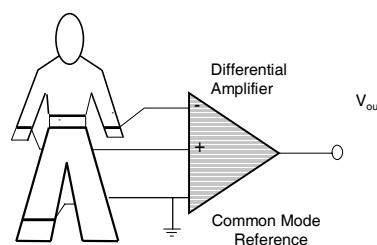
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Block Diagram of a general purpose bioelectric signal acquisition system



Typical configuration for the measurement of bio-potentials

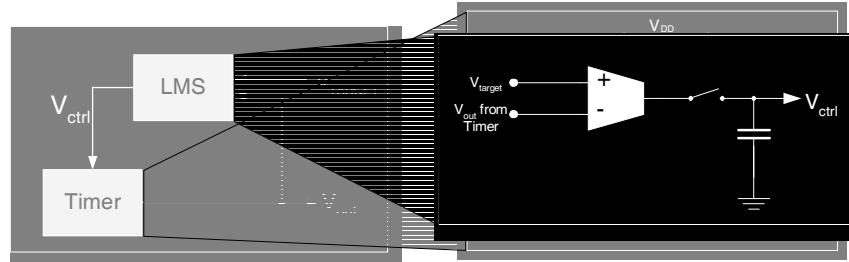
Parameter	Typical range
Gain	1-1000
Bandwidth	0.1 Hz-10KHz
Dynamic Range (DR)	60dB-100dB
CMRR	<b>80-140dB</b>
Zinput	10MΩ-1GΩ at 60Hz
Vnoise	<10nV/√Hz
Inoise	<1μA/√Hz

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## Continuous-Time Linear Ramp Implementation LMS Integrator



- The Timer is a cascode current source with  $V_{ctrl}$  controlling the gate voltage of the current source transistor
- The LMS block is an OTA-C integrator with a switch to control the charging of the capacitor

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Receivers and Transmitters in wireless applications -- used in PLL and for image rejection



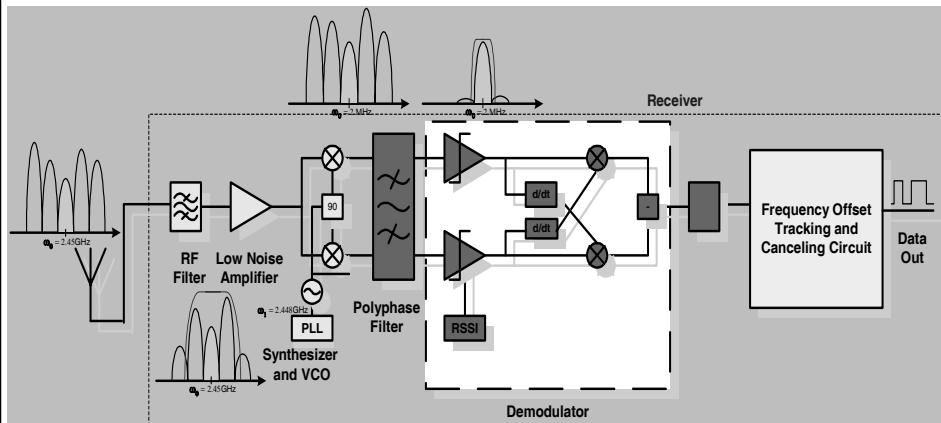
6185i digital cell phone from Nokia.

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# Low-IF Bluetooth Receiver



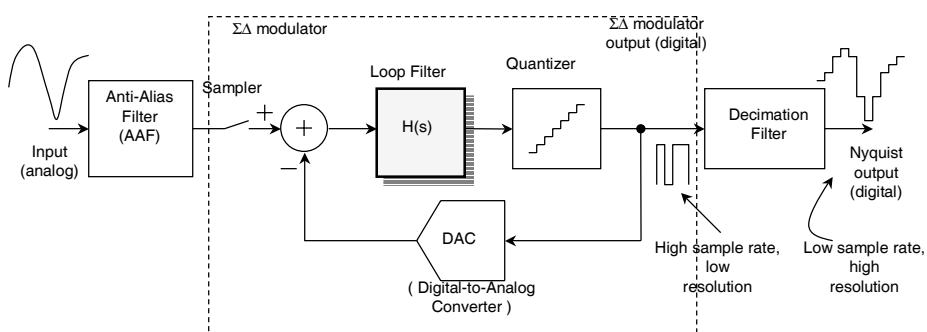
- Active polyphase filter is used to reject image and select channel.

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## Sigma-Delta Oversampled A/D Conversion



Functional level diagram of a general continuous-time sigma-delta oversampled analog-to-digital converter

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# A family of Transconductances for different frequency ranges applications.

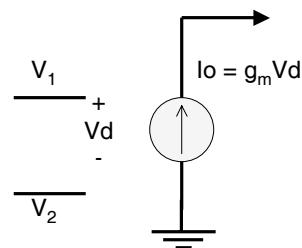
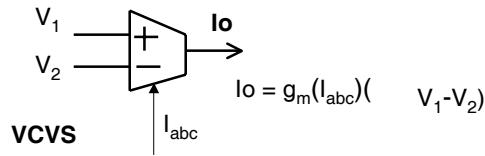
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## OPERATIONAL TRANSCONDUCTANCE AMPLIFIER (OTA)

First commercial OTA produced by RCA in 1969, i.e., CA3080



The transconductance gain "gm" is a function of the bias current  $I_{abc}$ .

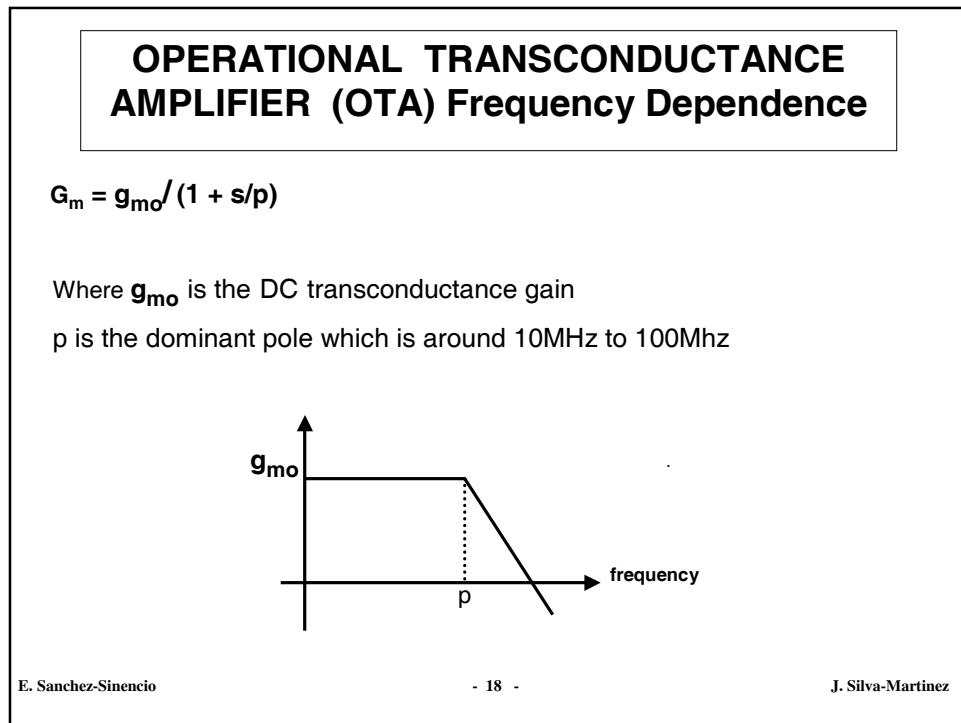
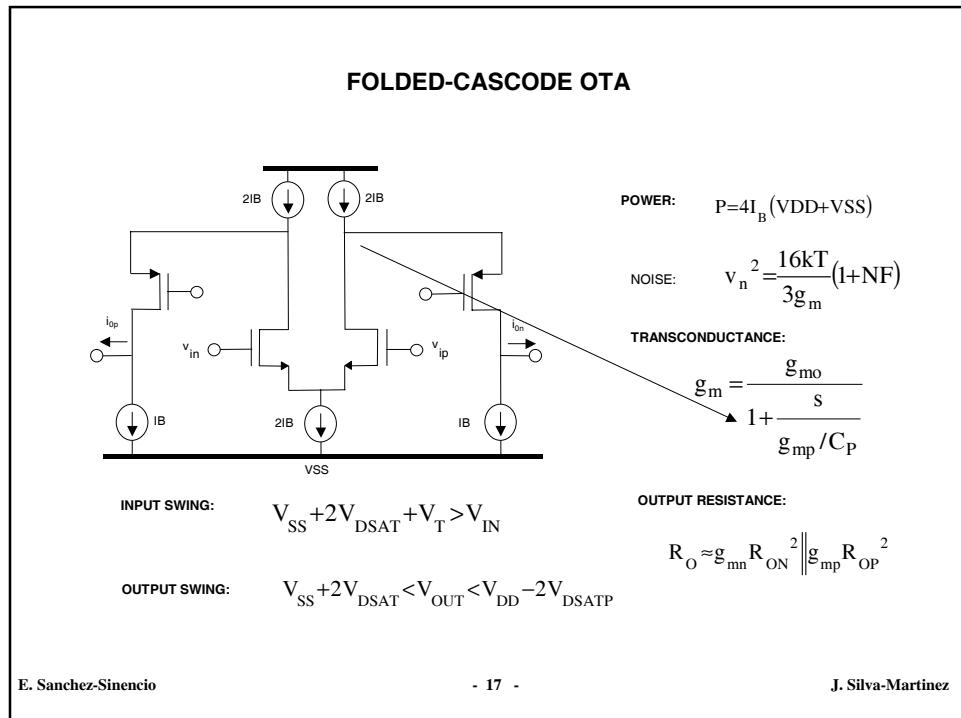
$$g_m = h_1 I_{abc} \text{ for bipolar and weak inversion MOSFETs}$$

$$g_m = h_2 [I_{abc}]^{1/2} \text{ for MOSFETs in saturation}$$

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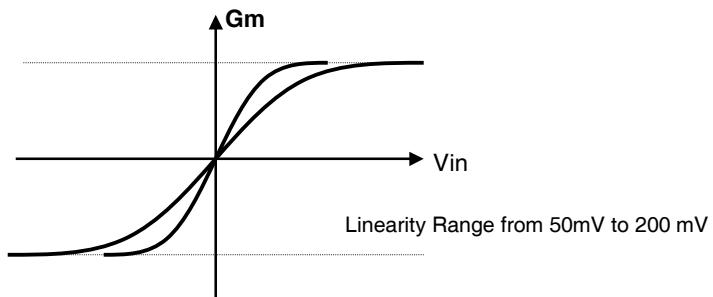
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## Issues about the OTA:

- Operated in open loop conditions
- High-Frequency Operation
- Poor Linearity Range

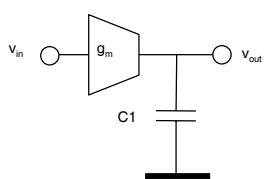


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## BASIC INTEGRATOR



$$g_m \equiv \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_T) \approx \sqrt{2\mu_n C_{ox} \frac{W}{L} I_D}$$

$$HD3 \approx \frac{1}{32} \left( \frac{V_{in}}{V_{GS} - V_T} \right)^2$$

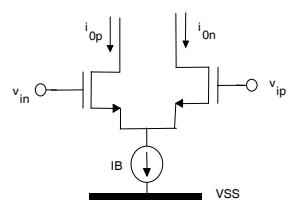
Mobility degradation:

$$\mu_n \equiv \left( \frac{\mu_0}{1 + \theta(V_{GS} - V_T)} \right) \left( \frac{1}{1 + \frac{\theta V_{in}}{1 + \theta(V_{GS} - V_T)}} \right)$$

$$HD3 \approx \frac{1}{4} \left( \frac{\theta V_{in}}{1 + \theta(V_{GS} - V_T)} \right)^2$$

NOISE:

$$v_n^2 = \frac{16kT}{3g_m} (1 + NF)$$



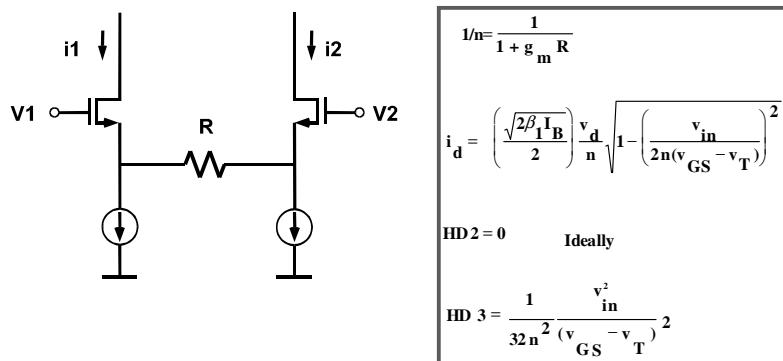
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### Differential Pair with Source Degeneration

Improved linearity

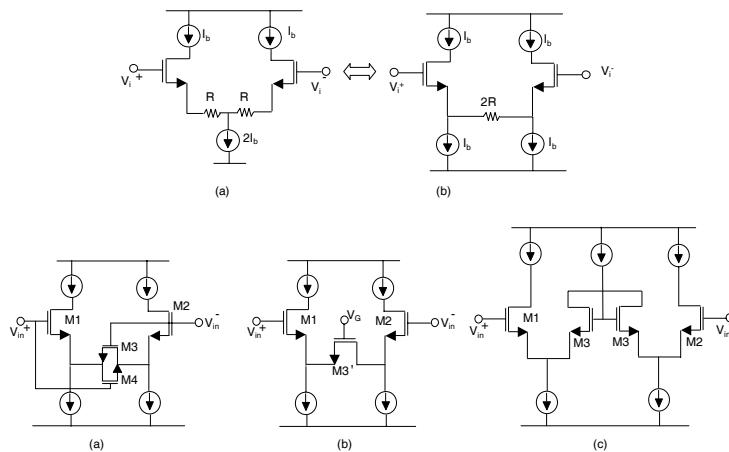


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### $g_m$ linearization schemes via source degeneration.



Active Source Degeneration topologies; (a) and (b) transistors biased on triode region and (c) with saturated transistors.

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## Properties of OTAs using source degeneration

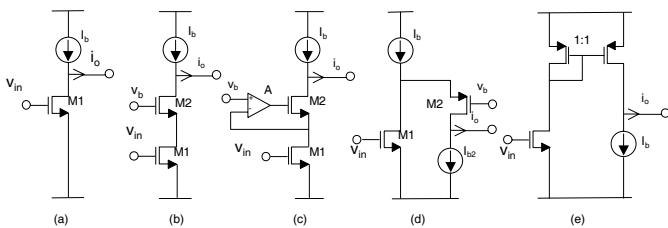
Reference/Figure	Transconductance	Properties
Fig. (a)	$\frac{g_{m1}}{1 + \frac{\beta_1}{4\beta_3}}$	Low sensitive to common-mode input signals. The linear range is limited to $V_{in} < V_{DSAT}$ , and THD=-50 dB. M1=M2, M3=M4
Fig. (b)	$\frac{g_{m1}}{1 + g_{m1}R}$ $R = 1/\mu_o C_{ox} (V_{gs} - V_T)$	Highly sensitive to common-mode input signals. For better linearity large $V_{GS3}$ voltages are required. Large tuning range if $V_G$ is used.
Fig. (c)	$\frac{g_{m1}}{1 + g_{m1}/g_{m3}}$ M1=M2	Low sensitive to common-mode input signals. Limited linearity improvement, HD3 reduces by -12 dB. More silicon area is required.

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## Single-input Transconductor (ST) Implementations



Single Input (a) Negative Simple Transconductor, (b) Cascode Transconductor, (c) Enhanced Transconductor, (d) Folded-Cascode Transconductor, (e) Positive Simple Transconductor.

- Observe that:  
 $g_m = f(I_b)$ , the exact relation is a function of the transistor region of operation.
- Note that output impedance of (a) and (e) are only  $1/g_{ds}$  and (b),(c) and (d) implementations have larger output impedances.

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## Properties of Simple (single input/ single output) Transconductors

Structure/ Figure	$R_{out}$	Min $V_{DD}^*$
Simple/1(a)	$\frac{1}{g_{ds1}}$	$\sqrt{\frac{2I_B}{k}} + V_{sat,I_B}$
Cascode/l(b)	$\frac{g_{m2}}{g_{ds1} g_{ds2}}$	$(1+m)\sqrt{\frac{2I_B}{k}} + V_{sat,I_B}$
Enhanced/l(c)	$\frac{A g_{m2}}{g_{ds1} g_{ds2}}$	$(1+m)\sqrt{\frac{2I_B}{k}} + V_{sat,I_B}$
Folded/l(d)	$\frac{g_{m2}}{g_{ds1} g_{ds2}}$	$\sqrt{\frac{2I_B}{k}} + V_{TP} + V_{sat,I_B}$

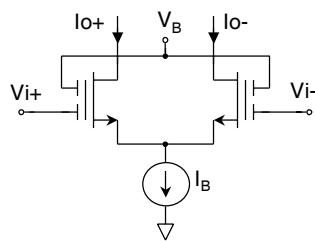
\* The bottom devices of the cascode pairs have an aspect ratio of  $(W/L)_1/(W/L)_2=m^2$ . K is a technological parameter determined by the mobility, and the gate oxide;  $V_{DSAT,1B}$  is the saturation voltage for the  $I_B$  current source

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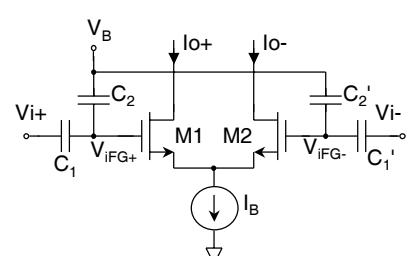
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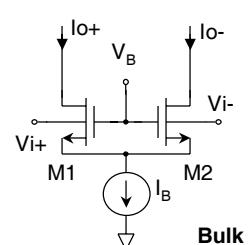
## Potential Solutions for Rail-to-Rail Amplifiers: OTAs suitable for Low Frequency Applications



FG DP Implementation



FG DP Equivalent circuit



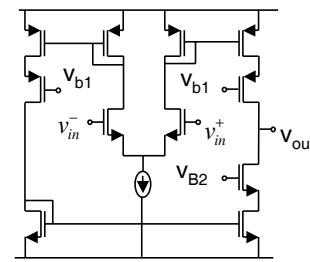
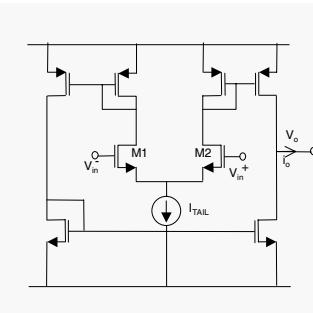
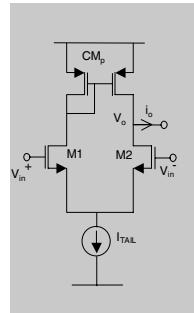
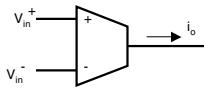
Bulk Driven DP

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### Three Conventional Differential Input –Single Ended OTAs



(a) Simple differential input OTA.

(b) Balanced OTA

(c) Cascode

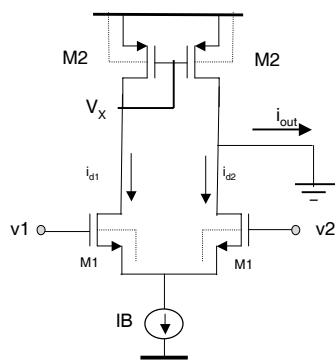
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### Simple OTA Design Equations

$$g_m = \mu_n C_{OX} \left( \frac{W}{L} \right) V_{DSAT}$$



Noise  $\Rightarrow g_m > g_{m,min}$

$$\text{GBW} = \frac{g_m}{C_L}$$

$$\omega_p = \frac{g_{mp}}{C_p} \equiv \mu_p \frac{V_{DSAT}}{2L_p^2}$$

$$R_{out} \equiv r_p \| r_n$$

$V_{DSAT}$  is limited !!

$$r_{ds} \equiv \frac{V_{early} L_p}{I_D}, \quad \omega_p \equiv \frac{g_{mp}}{2C_{GSP}} \equiv \frac{\mu_p V_{DSAT} p}{2L_p^2}$$

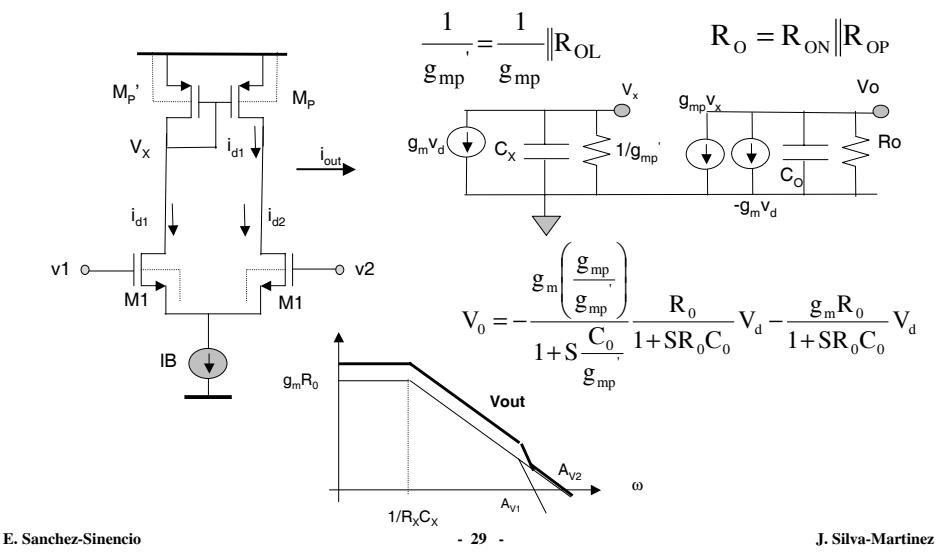
$$\text{Noise}(V_{RMS}) = \sqrt{\frac{16kT}{3}} \sqrt{\frac{1}{g_{m1}}} \sqrt{1 + \frac{g_{m2}}{g_{m1}}} (\sqrt{\text{BW}})$$

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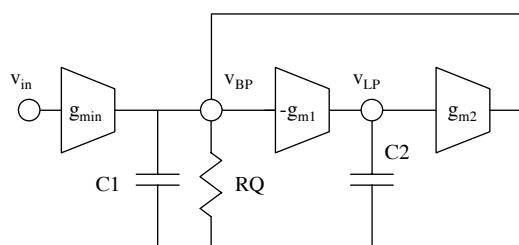
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## Simple OTA Frequency Response



## SECOND-ORDER FILTER

Bandpass output:



$$\omega_0 = \sqrt{\frac{g_{m1} g_{m2}}{C_1 C_2}}$$

$$Q = \sqrt{\frac{C_1 (g_{m1} g_{m2} R_Q^2)}{C_2}}$$

N= number of OTAs

$$A_{Vpeak} = g_{min} R_Q$$

$$IM3 \equiv \frac{3\sqrt{N}}{32} \left( \frac{V_{in}}{V_{GS} - V_T} \right)^2$$

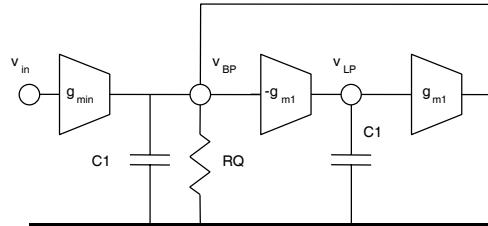
$$g_m \equiv \mu_n C_{OX} \frac{W}{L} (V_{GS} - V_T) \equiv \sqrt{2 \mu_n C_{OX} \frac{W}{L} I_D}$$

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## SECOND-ORDER FILTER



$$IM3 \equiv \frac{3}{32} \left( \frac{V_{in}}{V_{GS} - V_T} \right)^2 (\sqrt{3})$$

INTEGRATED NOISE:

$$v_{eq,in} (\text{RMS}) \equiv \sqrt{\frac{8kT}{3\pi C1}} * \sqrt{\frac{1}{R_Q g_{min}}} * \sqrt{1 + NF_{in} + \frac{g_{m1}}{g_{min}} (2 + NF_1 + NF_2) + \frac{0.75}{R_Q g_{min}}}$$

Tradeoff:

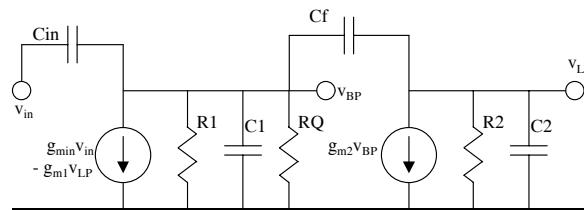
Large gain reduces the noise level but increases the harmonic distortions.

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## Output resistance effects



$$\omega_0 \approx \omega_{0\text{ideal}} \sqrt{1 + \frac{1}{Q_{\text{ideal}} A_V}}$$

CENTER FREQUENCY ISITTLE SENSITIVE TO  $A_V$   
BW IS QUITE SENSITIVE TO  $A_V$

$$BW \equiv BW_{\text{ideal}} \left( 1 + 2 \frac{Q}{A_V} \right)$$

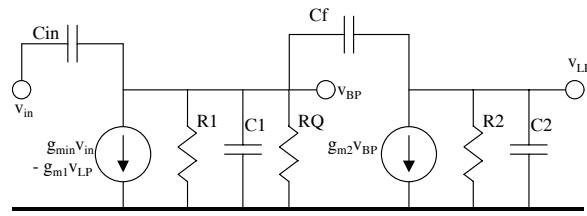
$A_V = g_{m1} R_1$  ( $R_1 = R_2$  OTA output resistance)

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## EFFECTS OF THE NON-DOMINANT POLE



Single pole:

$$g_m \approx \frac{g_{m0}}{1 + \frac{s}{\omega_{p1}}}$$

$$\omega_0 = \omega_{0\text{ideal}} \sqrt{\frac{1}{1 + \frac{2BW_{\text{ideal}}}{\omega_{p1}}}}$$

Sensitive

$$BW = BW_{\text{ideal}} \frac{1 - 2Q_{\text{ideal}} \frac{\omega_{0\text{ideal}}}{\omega_{p1}}}{1 + \frac{2BW_{\text{ideal}}}{\omega_{p1}}}$$

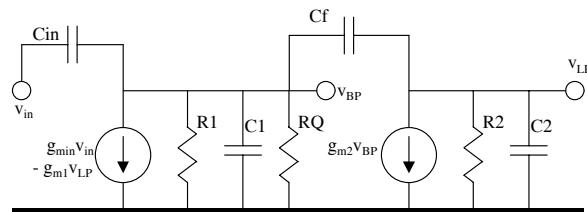
Quite sensitive

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## PARASITIC CAPACITORS



C1 and C2 are affected by the grounded parasitic capacitors (partially corrected by the automatic tuning system)

$$\omega_0 = \omega_{0\text{ideal}} \sqrt{\frac{C_1 C_2}{(C_1 + C_{\text{in}})(C_2 + C_f) + C_2 C_f}}$$

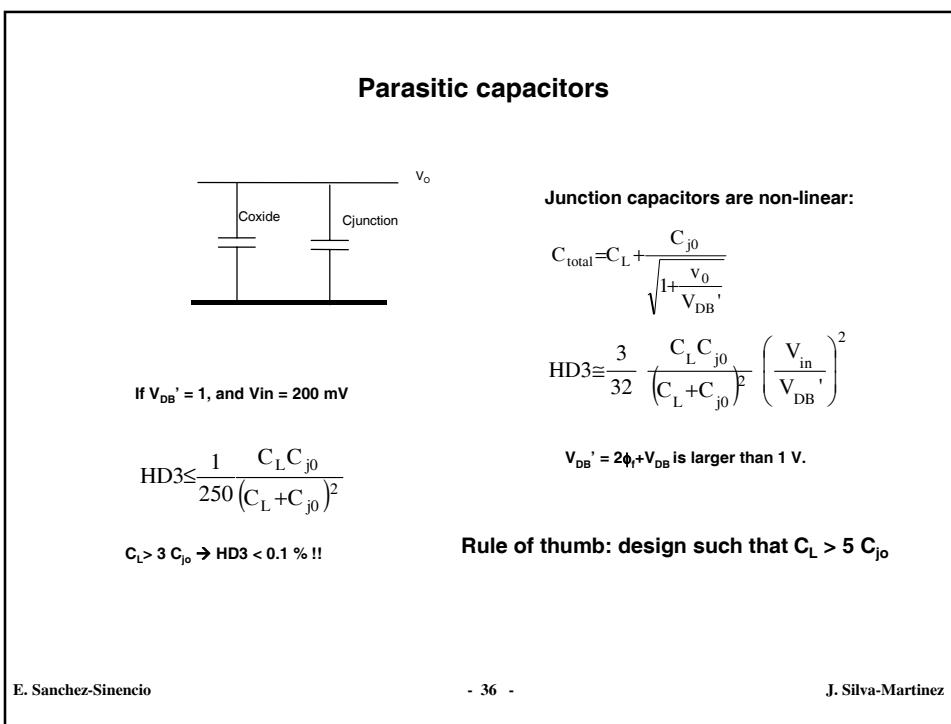
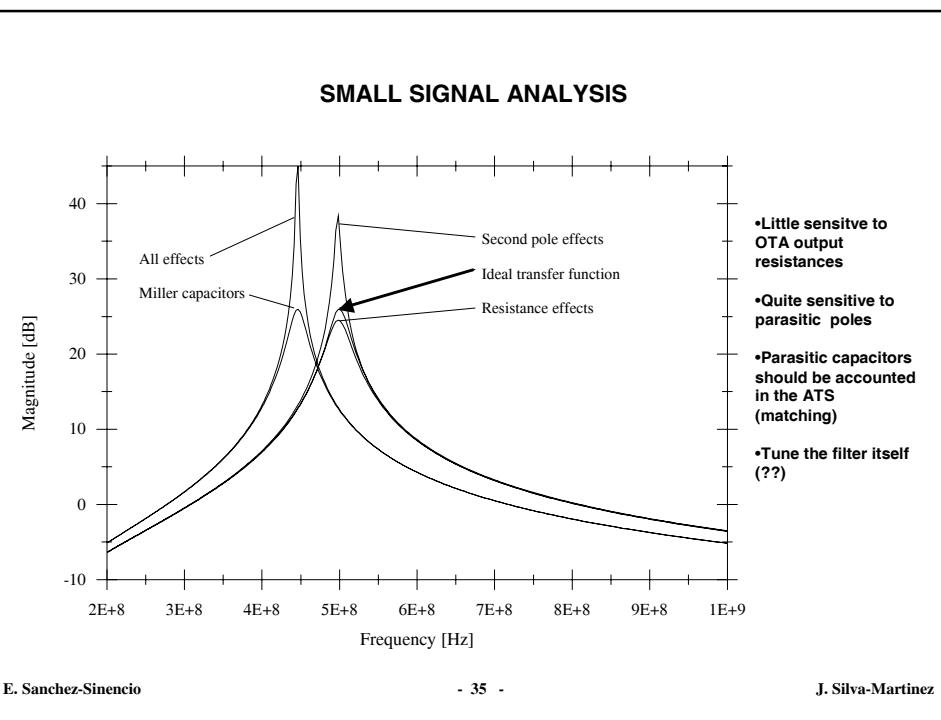
$C_{\text{in}}$  introduces a high frequency zero

$$BW = BW_{\text{ideal}} \frac{(C_1) \left( C_2 + C_f + \frac{g_{m2} - g_{m1}}{g_i} C_f \right)}{(C_1 + C_{\text{in}})(C_2 + C_f) + C_2 C_f}$$

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## VERY LOW FREQUENCY FILTERS

**The Design of Analog Circuits below 100 Hz  
is not trivial**

- **RC > 0.001 sec**
  - if C = 10 pF then R > 100 MOHMS
  - for a 1 Hz filter (pace makers and other applications)
  - C = 10 pF, R = 628 GOHMS (Gm = 2 pA/V)
  - C = 1000 pF, R = 6.28 GOHMS (Gm = 2 nA/V)

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**WE NEED SMALL G<sub>M</sub>**

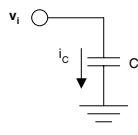
**For the basic OTA-C integrator,**

$$\tau = \frac{C_L}{g_m} \Leftrightarrow f = \frac{g_m}{2\pi C_L}$$

- We need very small gm for very low frequency applications
- As an example, for  $\tau = 1s$  and  $g_m = 16nA/V$ ,  $C_L = 1.6 nF$  !!
- For a POLY-I POLY-II Capacitor ( $C/A \sim 600 aF/\mu m^2$ ) in the AMI 1.2 $\mu$  process, this means a Si area of  $2.7 mm^2$  !! => Impractical for IC's ( Tiny chip area  $\sim 4 mm^2$  )

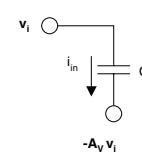
## IMPEDANCE SCALERS for realization of very large time constants

Single capacitor



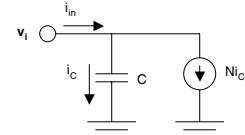
$$i_C = (sC)v_i$$

Voltage amplifier



$$i_{in} = (sC(1 + A_V))v_i$$

Current amplifier



$$i_{in} = (sC(1 + N))v_i$$

Remarks:

▫ Voltage amplification is useless for low-voltage continuous-time applications.

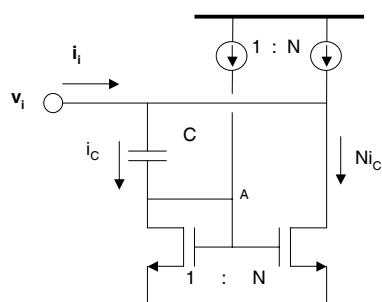
▫ Impedance scaler based on current amplification is precise for moderately N.

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## CAPACITOR MULTIPLIER: CIRCUIT IMPLEMENTATION



$$\frac{v_i}{i_i} = \frac{v_i}{(N+1)i_C}$$

$$Z_{eq} = \frac{1}{s[(N+1)C]}$$

The following conditions must be satisfied:

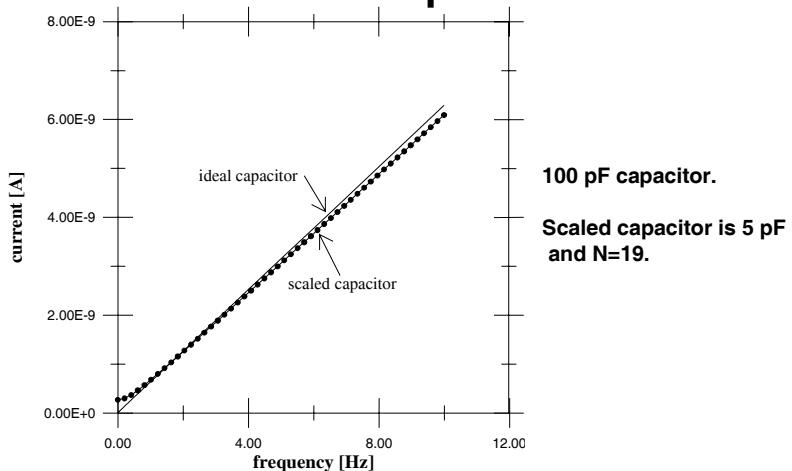
- Low impedance at node A
- Transistor output resistance might be neglected
- Current gain is precise

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## DESIGN EXAMPLE: Capacitor Multiplier

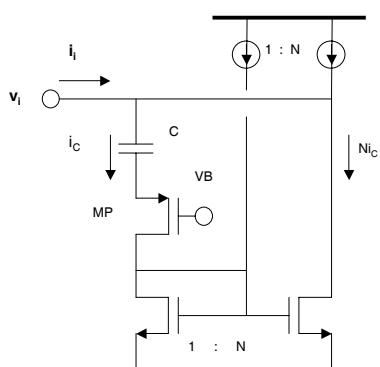


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## IMPROVING ITS FREQUENCY RESPONSE



Cascode transistor improves frequency response

Design procedure:

MP is optimized for frequency.

N-type transistors are optimized for precision.

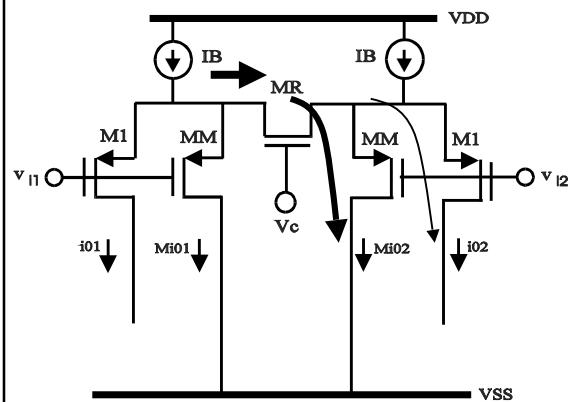
The loop must be stable

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## CURRENT DIVISION PRINCIPLE PLUS SOURCE DEGENERATION TRANSCONDUCTANCE



$$HDn \propto \left( \frac{v_{i1} - v_{i2}}{V_{GS} - V_T} \right)^n$$

$$\frac{i_{01} - i_{02}}{v_{i1} - v_{i2}} = \frac{2\mu C_{OX} W_R}{M+1} (V_{GS} - V_T) \cdot \frac{L_R}{L_R}$$

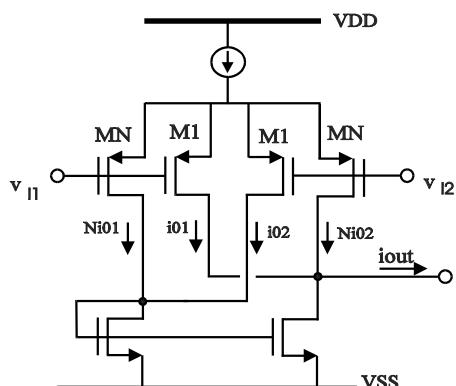
$$M = \left( \frac{W}{L} \right)_m / \left( \frac{W}{L} \right)_1$$

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## CURRENT CANCELLATION PRINCIPLE



$$G_m = \frac{N-1}{N+1} g_{m1}$$

PARTIAL POSITIVE FEEDBACK !

LINEAR RANGE IS LIMITED!

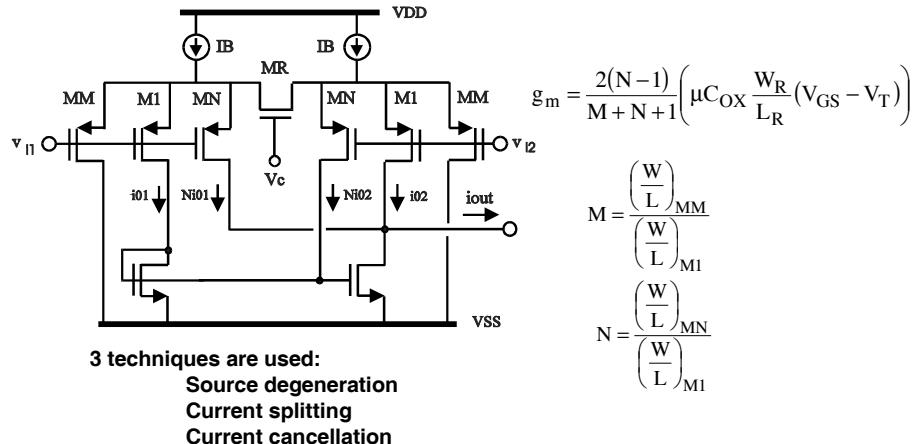
GOOD NOISE PERFORMANCE

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## OTA FOR VERY LOW-FREQUENCY APPLICATIONS

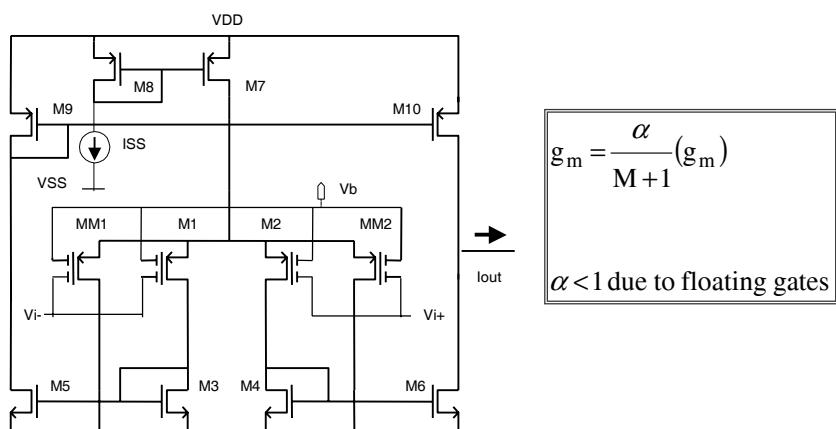


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## Floating Gate plus Current Division OTA



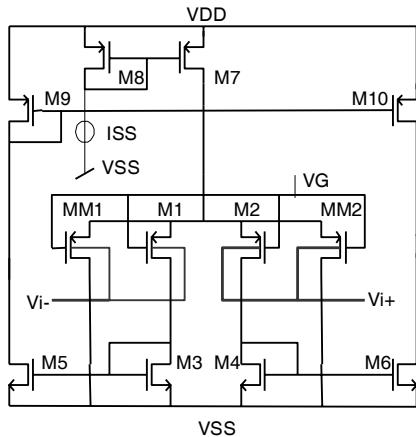
Floating gates improve linearity but:  
DC-offset might be an issue  
Signal attenuation: noise might be an issue as well

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## Bulk Driven plus Current Deviation OTA



Input impedance is not very large:  
important issue for low frequency  
applications.

Gm-bulk is 4-5 times smaller than  
normal gm.

Large parasitic capacitors (well-  
substrate)

PSRR might be limited

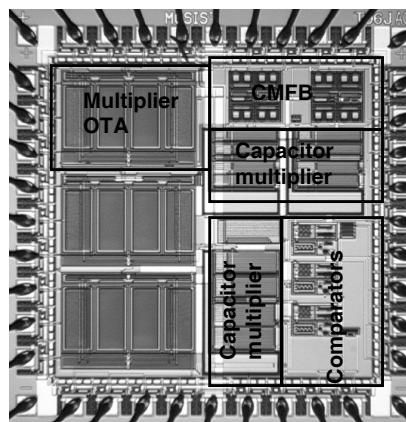
**Bulk-Driven & Current splitting**

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## Chip microphotograph of a sub-hertz filter/oscillator (JSSC-Aug-2002).

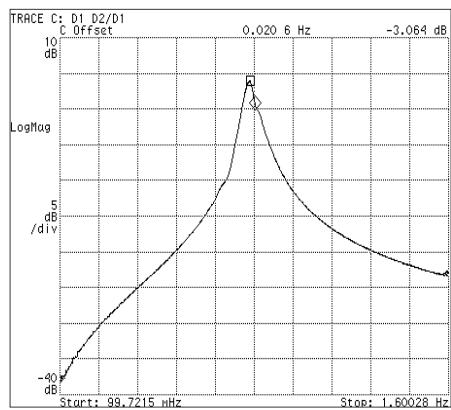


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## Experimental results for a 0.8 Hz BPF (0.5 $\mu$ m CMOS technology).

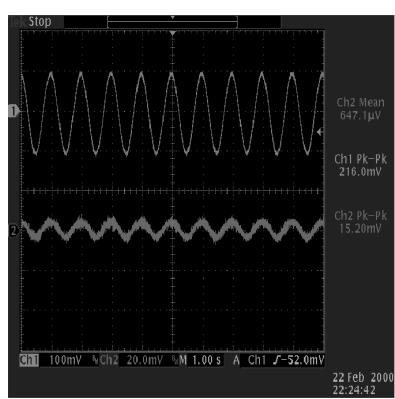


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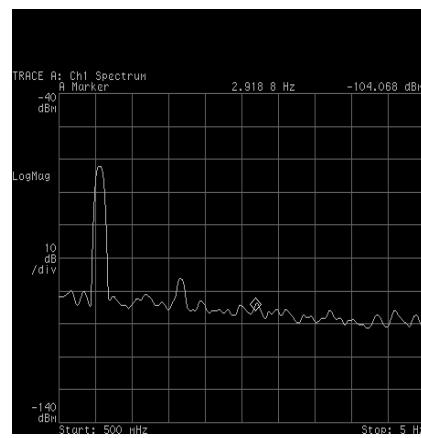
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## BULK DRIVEN OTA EXPERIMENTAL RESULTS (0.5 $\mu$ m)



Input Ch1 214mVpp @ 1 Hz  
Output Ch2 15.2mVpp



THD ~ -39dBm ~ 1.1% @214mVpp, 1Hz

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## EXPERIMENTAL RESULTS FOR THE DIFFERENT OTA DESIGNS

PARAMETER	REFERENCE	SD+CD	FG+CD	BD+CD
$G_M$ (nA/V)	9.4	9.3	9.2	9.4
$HD_3$ (%)	0.9@162mV <sub>pp</sub>	1.0@242mV <sub>pp</sub>	1.1@330mV <sub>pp</sub>	0.9@900mV <sub>pp</sub>
Input noise ( $\mu$ Vrms)	18.1	26.1	39.1	104.7
SNR@1% $HD_3$ (dB)	69.9	70.3	69.5	69.6
$I_{BIAS}$ (nA)	2.6	120	232	560

Key:

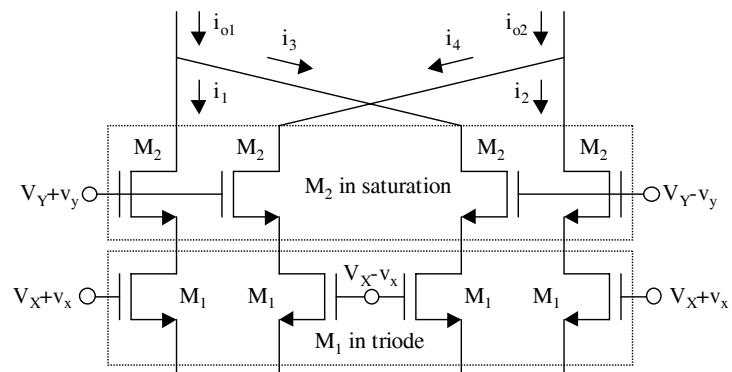
SD source degeneration  
 CD current division  
 FG floating gate  
 BD bulk driven

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## OTA for Low-Frequency Applications with Current cancellation techniques

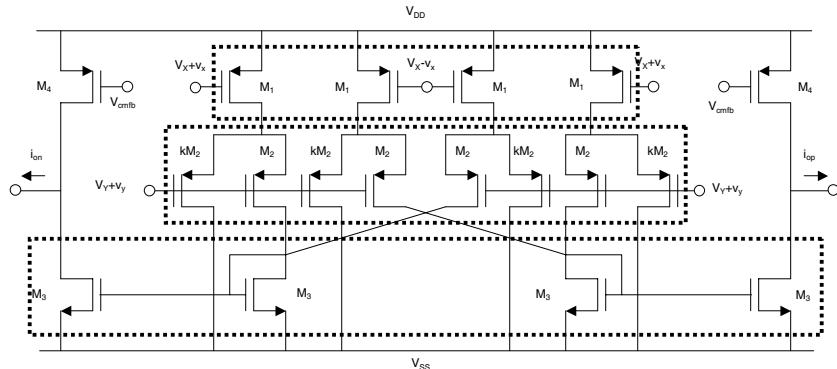


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## OTA with current splitting and cancellation techniques



Basic circuit is an analog multiplier

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## Fully Differential and Pseudo Differential OTAs

Common - Mode Feedforward and  
Feedback strategies needed for  
differential output filters

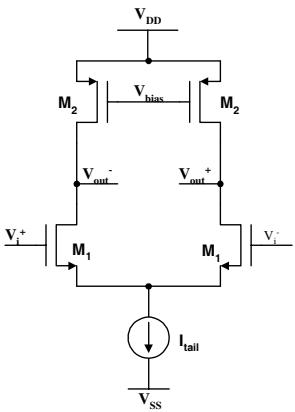
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## Fully Differential OTA Characteristics

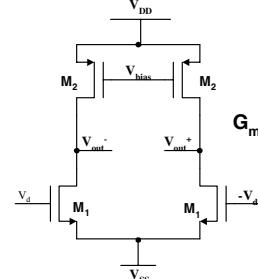
Simple Differential OTA  
With tail Current Source



- ✗ Limited linear input range
- ✗ Limited tuning range

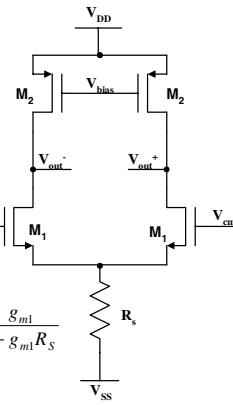
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Differential-Mode



- ✓ Reasonable Common-mode gain
- ✓ Reasonable PSRR

Common-Mode



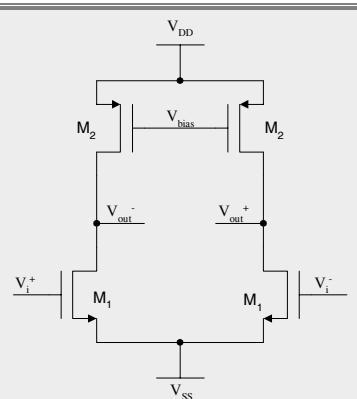
$$G_m = g_{m1}$$

$$G_m = \frac{g_{m1}}{1 + g_{m1}R_s}$$

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## Pseudo Differential Transconductance



Simple Pseudo  
Differential OTA

### Advantages

- ✓ Suitability for low voltage
- ✓ Wider common-mode input range

### Disadvantages

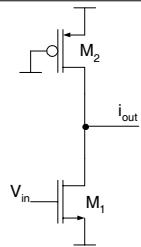
- ✗ Poor common-mode gain
- $A_{CM} = A_{DM} >> 1$
- ✗ Poor PSRR
- ✗ Low output impedance
- ✗ Need for fast and strong Extra CMFB Circuit to
  - (1) Fix output common-mode voltage
  - (2) Suppress common-mode signals

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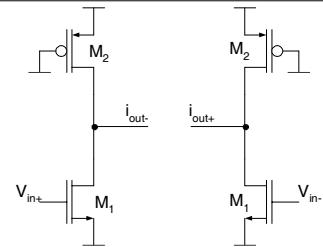
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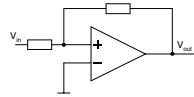
## Derivation of CMFF Pseudo-differential OTA



Single ended OTA circuit

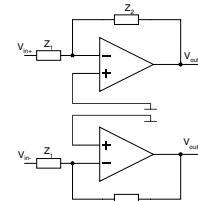


Circuit of OTA for differential input



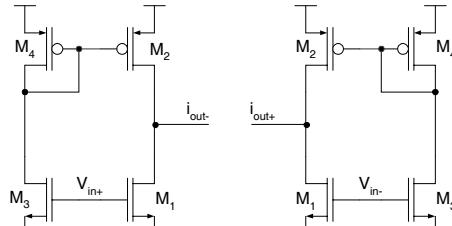
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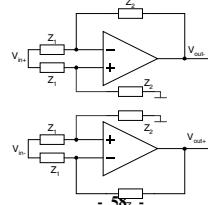


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## CMFF Techniques for Pseudo-differential OTAS



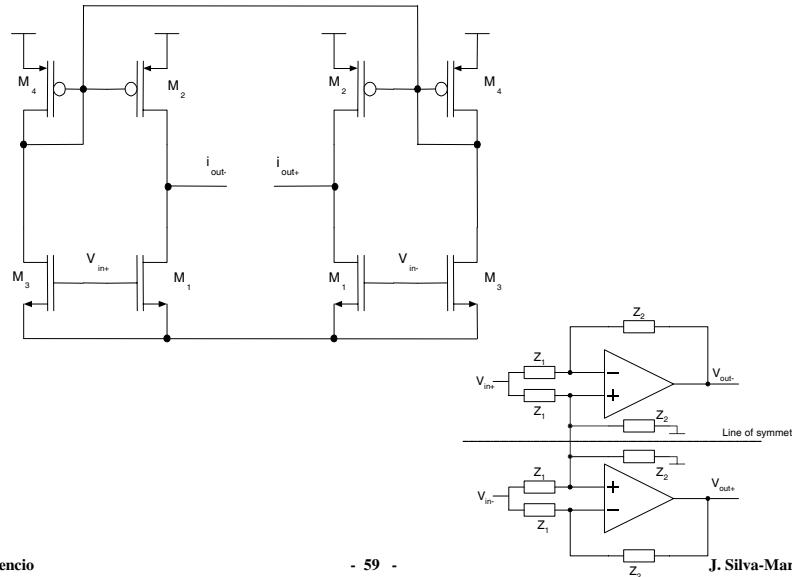
Circuit of OTA for common mode signals



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### Fully-balanced, fully-symmetric CMFF OTA

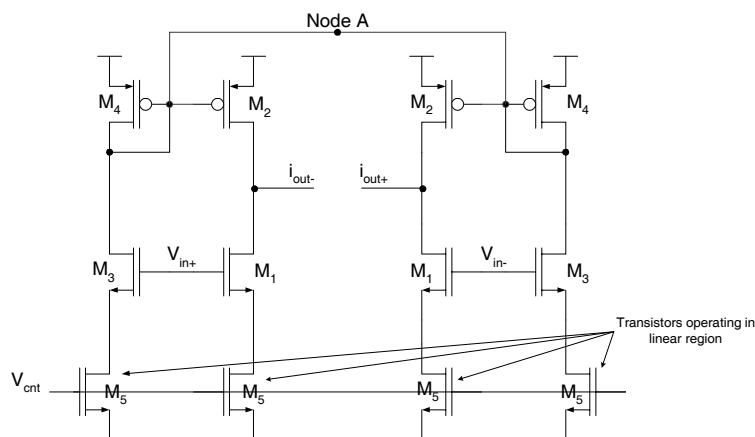


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### OTA with improved linearity



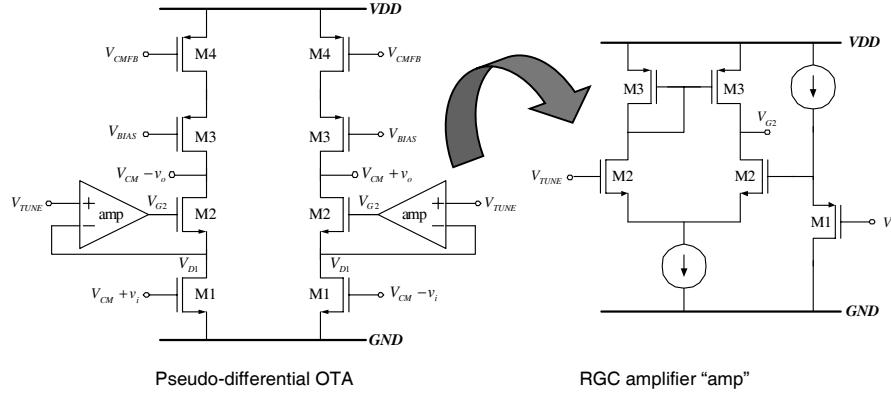
### Fully-balanced, fully-symmetric, pseudo differential CMFF OTA

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## Pseudo-Differential OTA with large output impedance



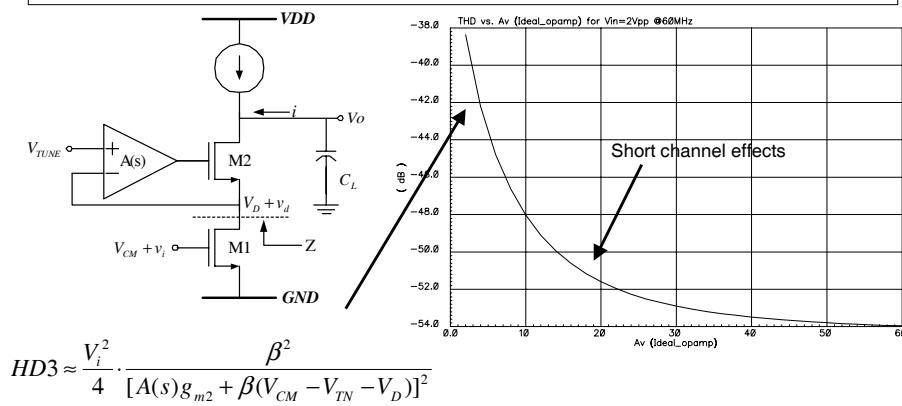
- Transistors M1 operate in Linear region: Wide linear range; Large tuning range;
- RGC loop: Fix  $V_{DS1}$  → provides better linearity.

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## OTA Design Issues: RGC Loop



- OTA Gm's Linearity: Limited by how well the drain voltage of the input transistor is fixed.

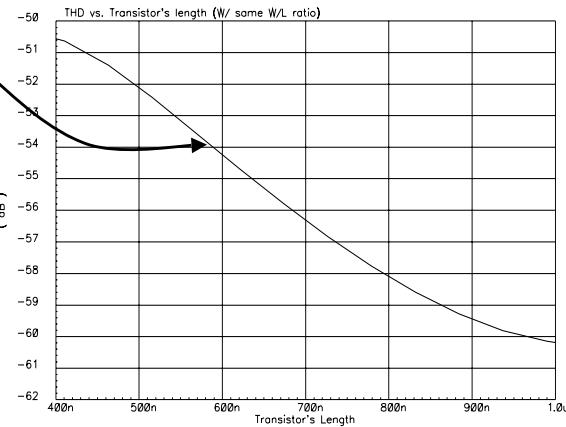
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## Design Issues: Short-Channel Effects

$$\mu_{\text{eff}} = \mu_0 \left( \frac{1}{1 + \theta(V_{GS} + v_{gs} - V_T)} \right) \cdot \left( \frac{1}{1 + \frac{1}{L\varepsilon_c} \cdot (V_{DS} + v_{ds})} \right)$$



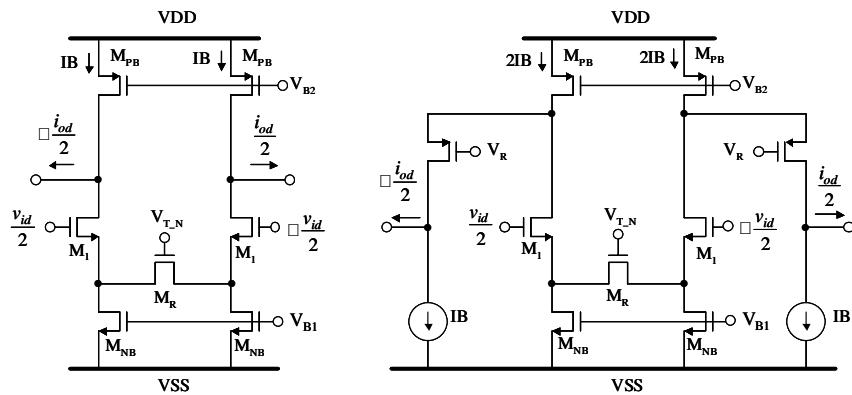
- Transversal electric field's effect is reduced by RGC loop;
- Trade-off: THD vs. Frequency response

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## TYPICAL OTA ARCHITECTURES WITH SOURCE DEGENERATION



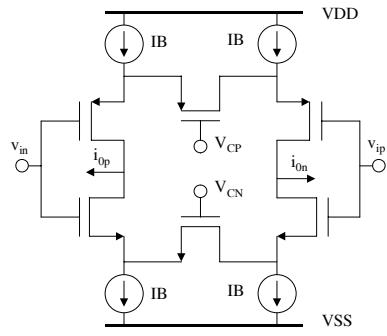
$$G_m = \frac{g_{m1}}{1+N_n} \sqrt{1 - \left( \frac{v_{id}}{2(1+N_n)V_{DSAT1}} \right)^2} = \frac{\mu_n C_{OX} V_{DSAT1} \left( \frac{W}{L} \right)_1}{1+N_n} \sqrt{1 - \left( \frac{v_{id}}{2(1+N_n)V_{DSAT1}} \right)^2}$$

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## OTA based on Complementary Differential Pairs



TRANSCONDUCTANCE:

$$G_m = \frac{g_{mn}}{1 + N_N} + \frac{g_{mp}}{1 + N_P}$$

DISTORTION:

$$HD3 \cong \frac{1}{32} \left( \frac{V_{in}}{(V_{GS} - V_T)(N+1)} \right)^2$$

OUTPUT RESISTANCE:

$$R_{out} \approx \frac{g_{mn} r_{on} R_N}{2} \parallel \frac{g_{mp} r_{op} R_P}{2}$$

OUTPUT SWING IS LIMITED:

$$V_{IN} - V_{TN} < V_{OUT} < V_{IN} + V_{TP}$$

DC GAIN:

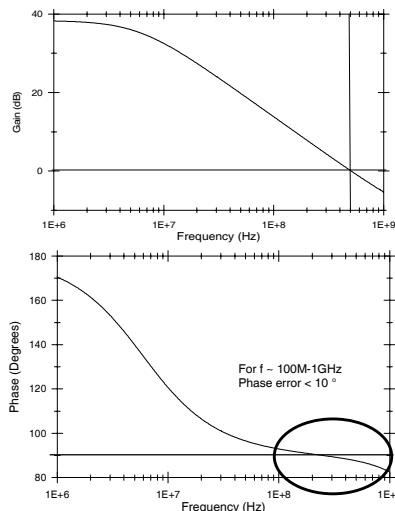
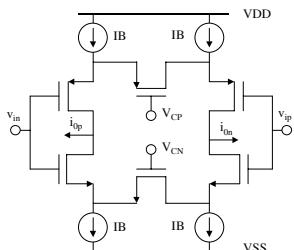
$$A_V \approx A_{VN}$$

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## OTA Results



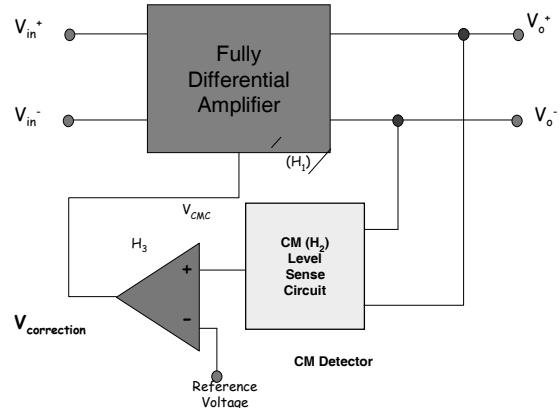
Transistor	W, L	Bias current
MP	200, 0.6 $\mu$ m	200 $\mu$ A
MCP	15, 0.6 $\mu$ m	0 $\mu$ A
MN	200, 0.6 $\mu$ m	200 $\mu$ A
MCN	30, 0.6 $\mu$ m	0 $\mu$ A

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# Common-Mode Feedback Circuits



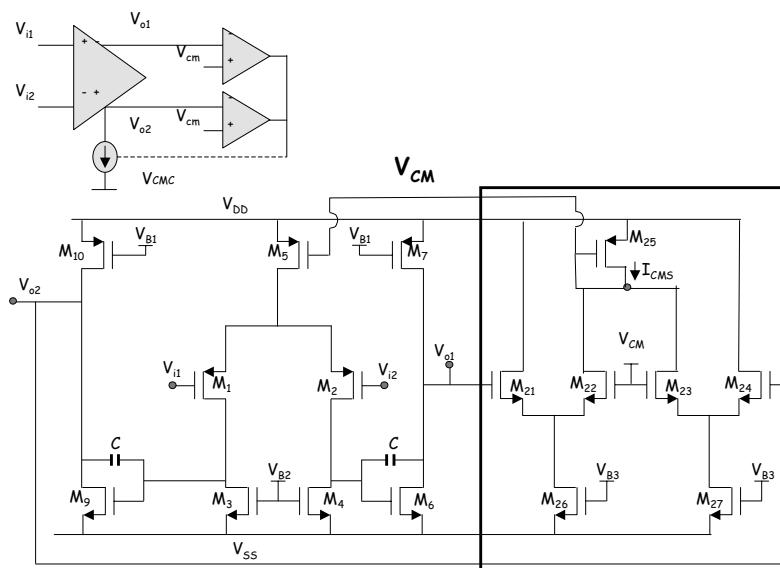
**Conceptual Architecture of  
Common-Mode Feedback Loop**

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## Example of a compensated Op Amp and a CM sense circuit



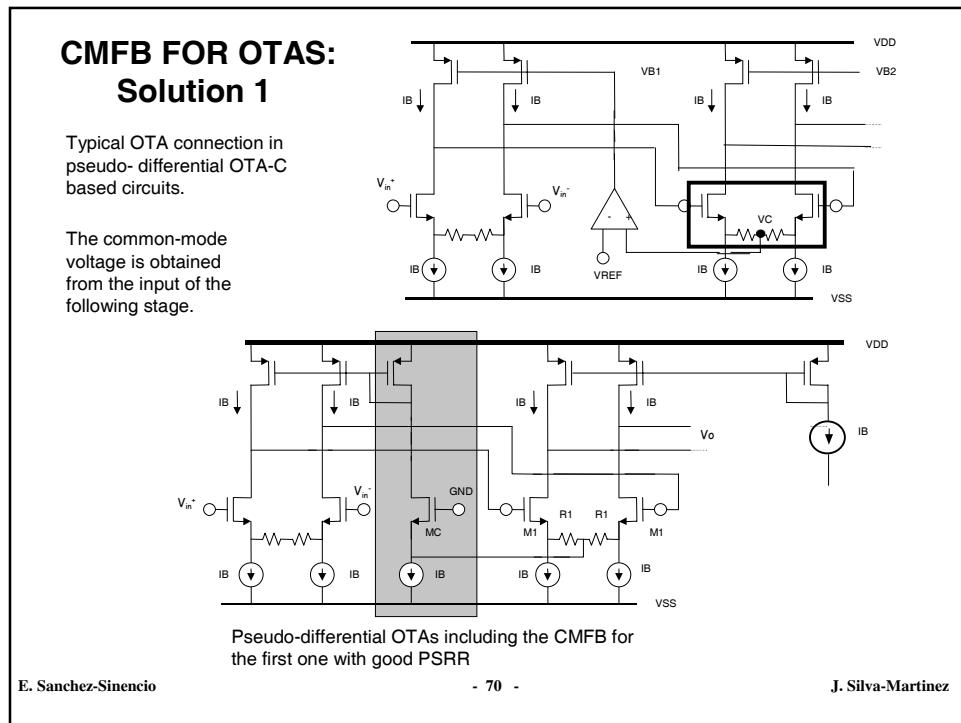
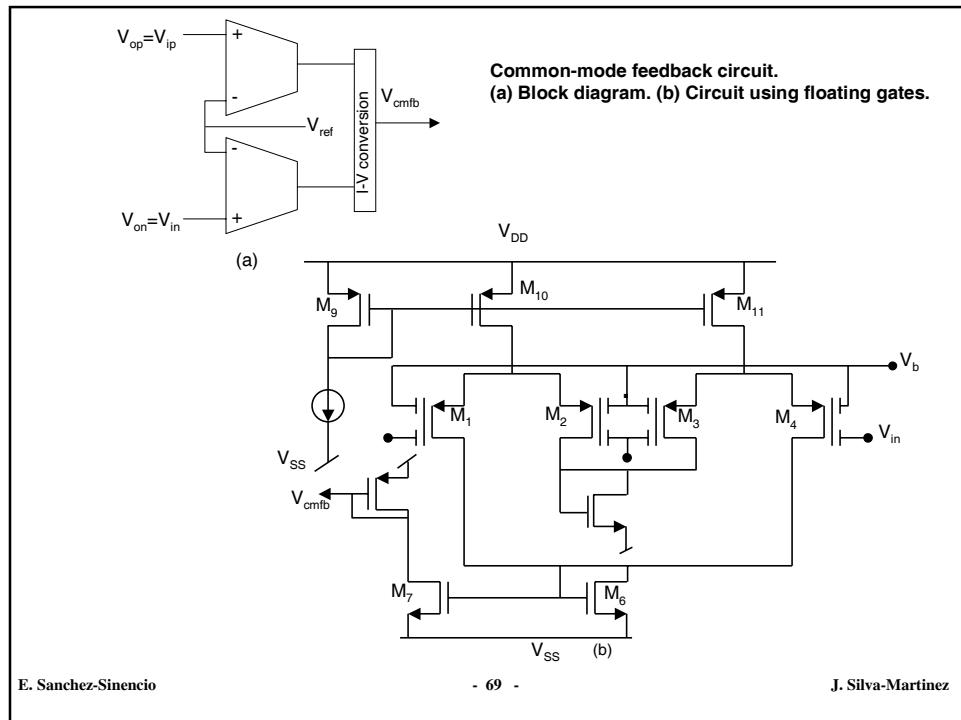
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**Op Amp**

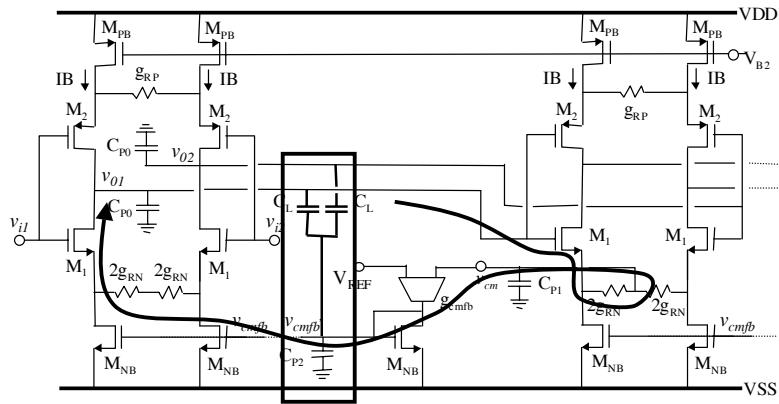
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**CM Detector**

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## COMMON-MODE FEEDBACK USING BYPASSING THE LOW-FREQUENCY SECTION

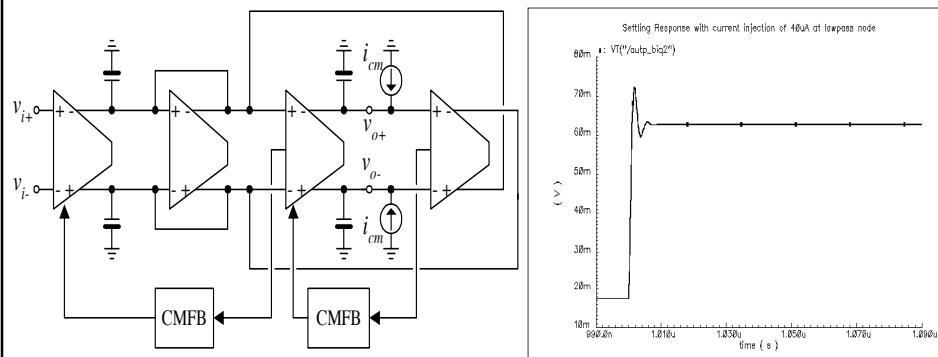


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## Testing the COMMON-MODE FEEDBACK in a biquadratic filter



Biquadratic section with a common-mode input current: pulse of 40 mA.

Pulse response of the CMFB embedded in a biquadratic section

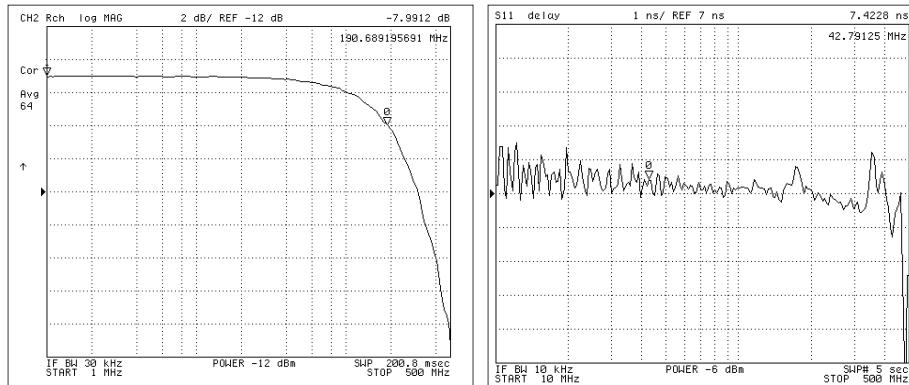
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## Frequency response

### Magnitude and Group Delay



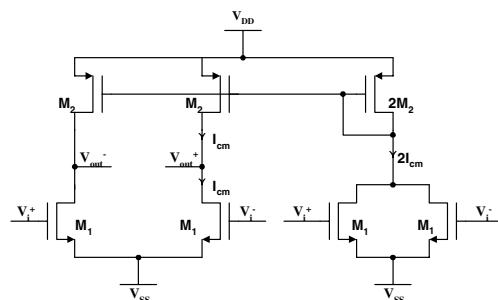
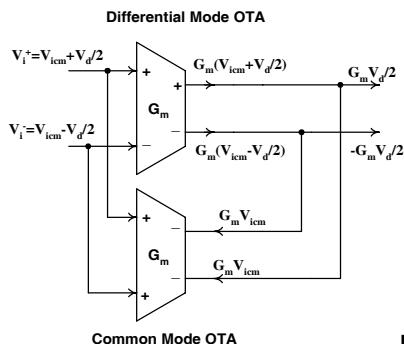
200 MHz linear phase filter: Group delay ripple is  $< \pm 1$  nsec up to 400 MHz

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## A PD Solution 2



Pseudo differential OTA With CMFF

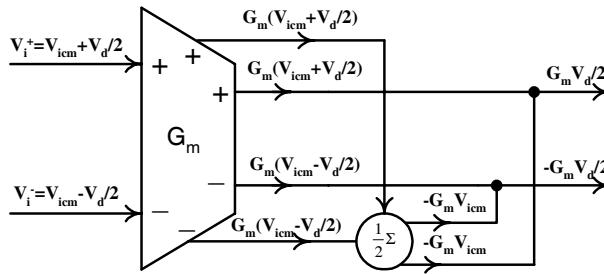
- ✓ CMFF is applied to cancel the common mode input signal
- ✗ Add load to the driving stage, input capacitance doubles
- ✗ CMFB is still needed

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### Proposed OTA Block Diagram (solution 3)



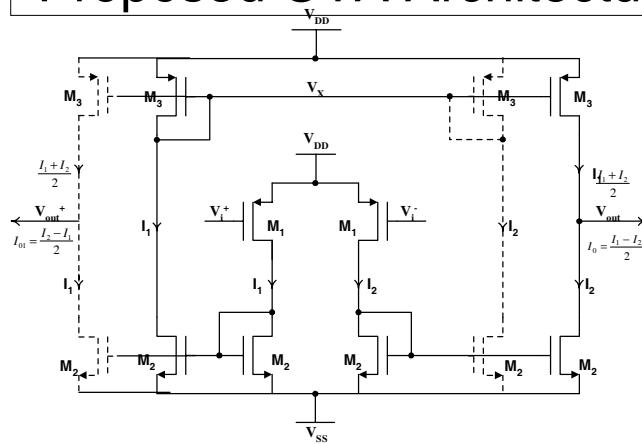
- Common-mode detection using the same differential transconductance by making copies of the current
- Input capacitance is not increased
- CMFF is inherently achieved
- CMFB can be easily arranged

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### Proposed OTA Architecture



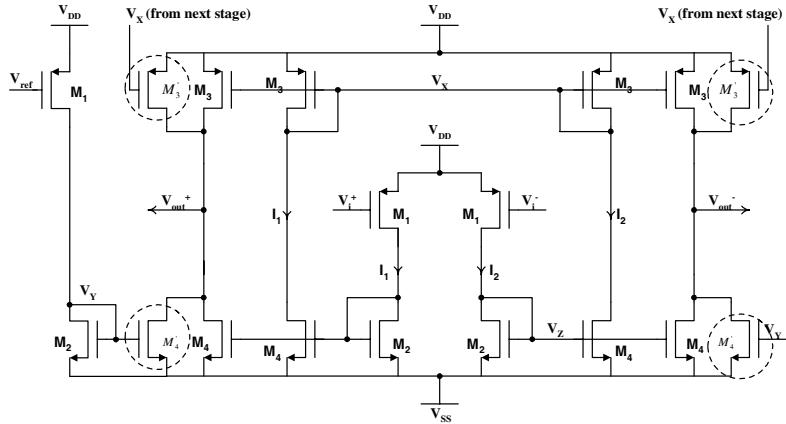
- Inherent common-mode detection
- Inherent common-mode Feedforward

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## Combine CMFB and CMFF



- ❑ CMFB is arranged exploiting the direct connection of the OTAs
- ❑ Avoid using a separate common-mode detector
- ❑ Differential-mode signals and common-mode signals share basically the same loop

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## Small Signal Analysis

- ❑ The path from the differential signal to the output encounters one pole
- ❑ The other path is a common-mode path

$$g_m(s) = \frac{i_{od}}{v_d} \cong g_{m1} \frac{g_{m2}}{g_{m2} + sC_Z} = \frac{g_{m1}}{1 + s/\omega_{nd}} \quad \omega_{nd} = \frac{g_{m2}}{C_Z}$$

$$\Delta\phi \cong -\tan^{-1}(\omega/\omega_{nd})$$

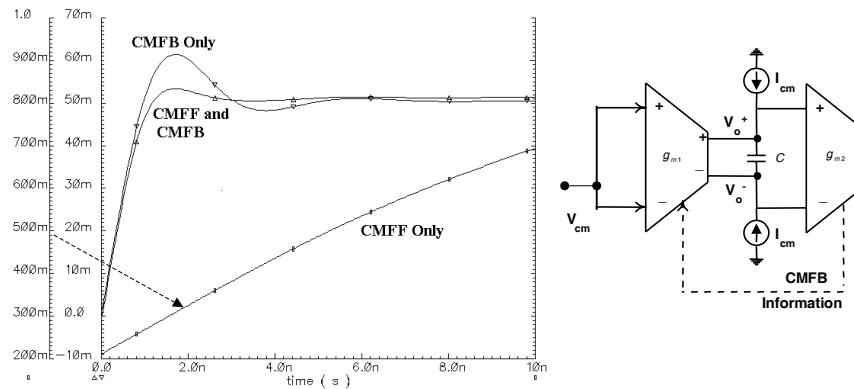
$$\min V_{DD} = \max \{ (V_{TN} + V_{ov1} + V_{ov2} + V_{peak}), (V_{TP} + V_{ov3} + V_{ov4}) \}$$

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## Simulation Results



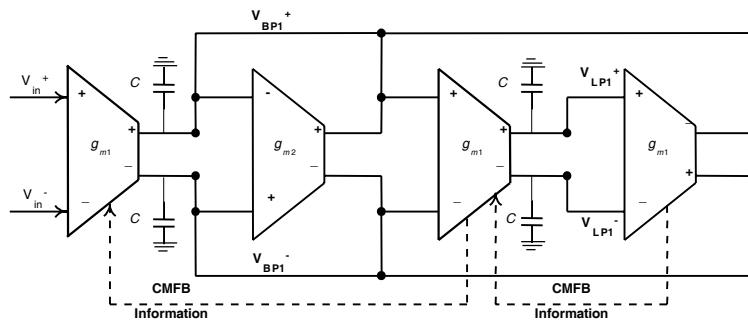
Output voltage applying common-mode current step ( $I_{cm}$ )

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## How to use OTAs as CM Detector?

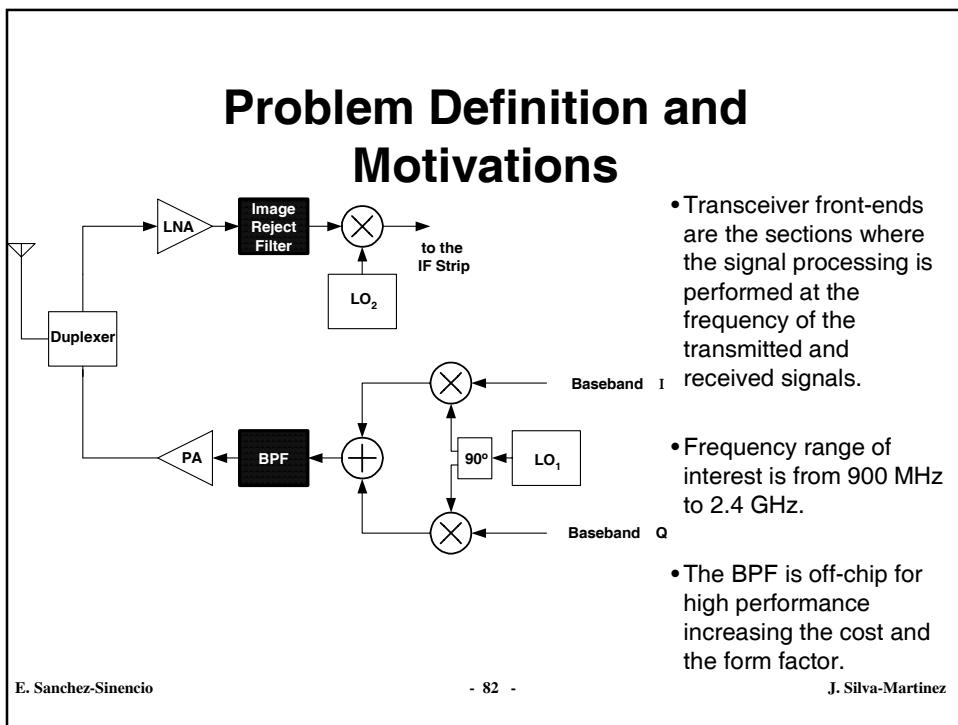
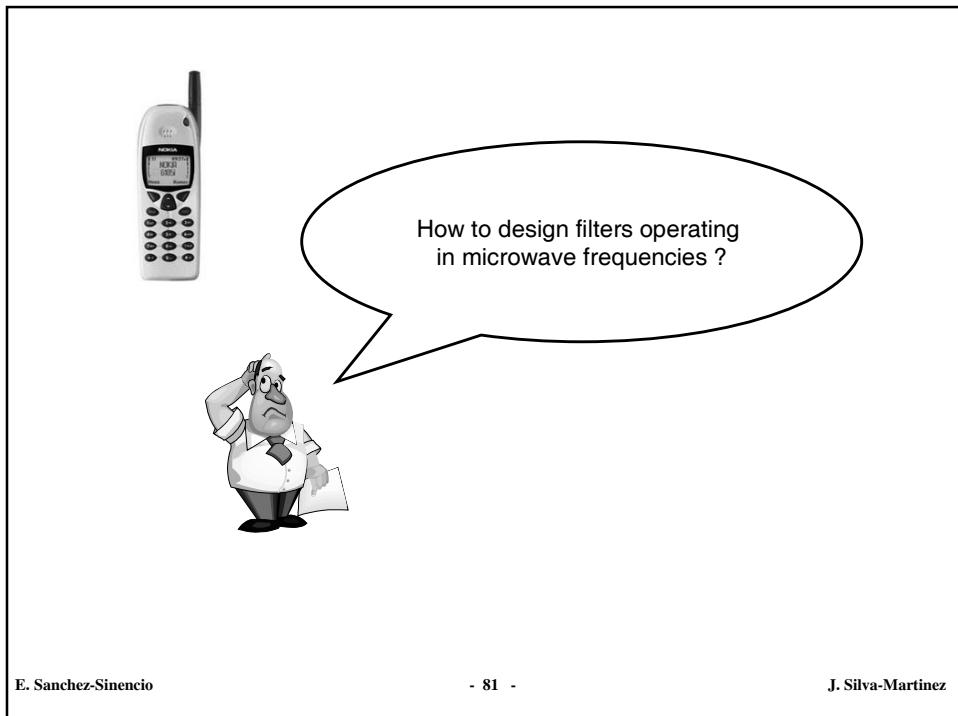


- A 2<sup>nd</sup> Order Filter is used as an example
- Exploit direct connection of the cascaded OTAs in the filter
- Differential OTA used as CM detector also

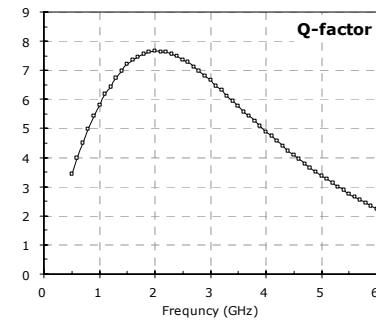
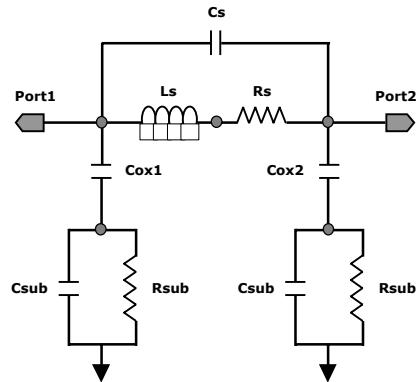
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## Equivalent Circuit & Calculation



**Parameter Calculation**

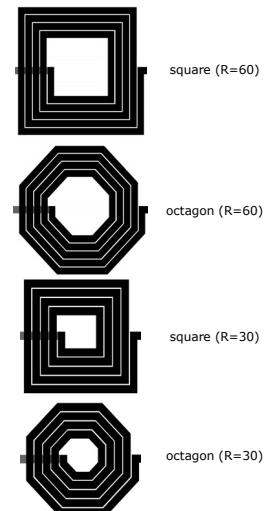
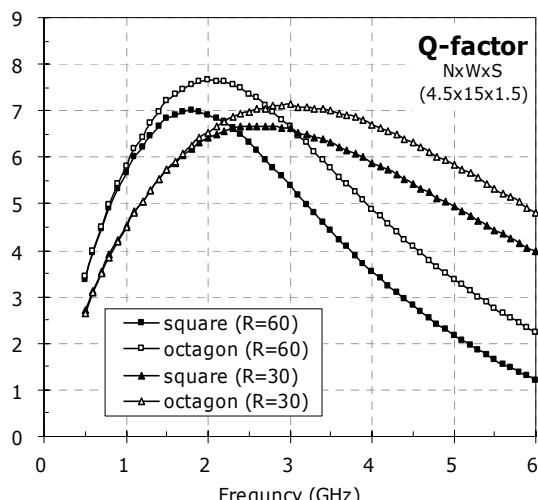
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## Layout Split 1

**Shape & Radius**

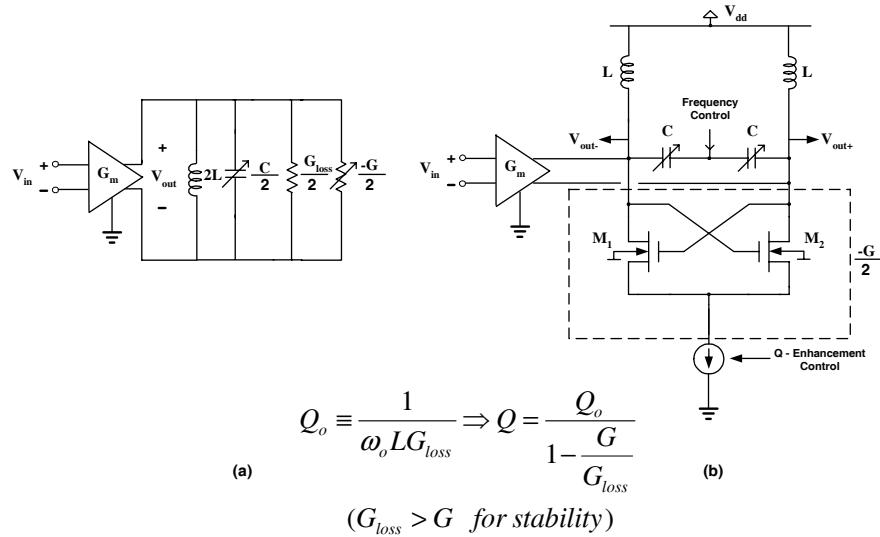


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## Q-Enhancement Bandpass Filter



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## A CMOS Programmable RF Bandpass Filter

- Programmable in:
- Peak Gain (not exploited previously)
  - Filter Q
  - Center Frequency

$$|H(j\omega_o)| \cong |G_m(j\omega_o) Q \omega_o L|$$

$$\omega_o \cong \frac{1}{\sqrt{LC}}$$

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## A CMOS Programmable Bandpass Filter

- The peak gain programmability through the input  $G_m$  stage.

$$|H(j\omega_o)| \equiv \left| \frac{G_m(j\omega_o)}{G_{loss} - G} \right| = |G_m(j\omega_o) Q \omega_o L|$$

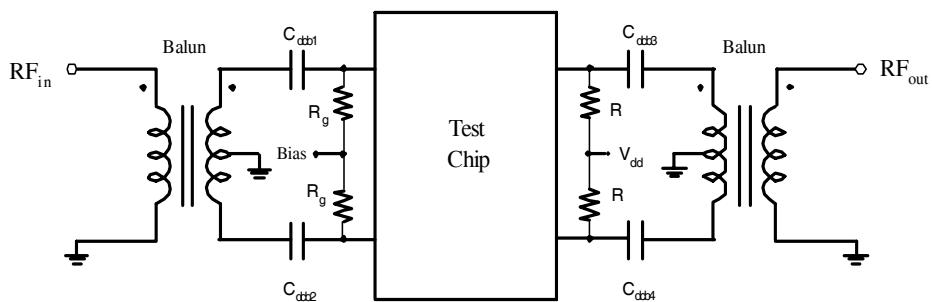
- Increasing Q also increases the peak gain.
- If  $\omega_o$  and Q are fixed, the peak gain can be modified through  $G_m$ .

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## The Test Setup



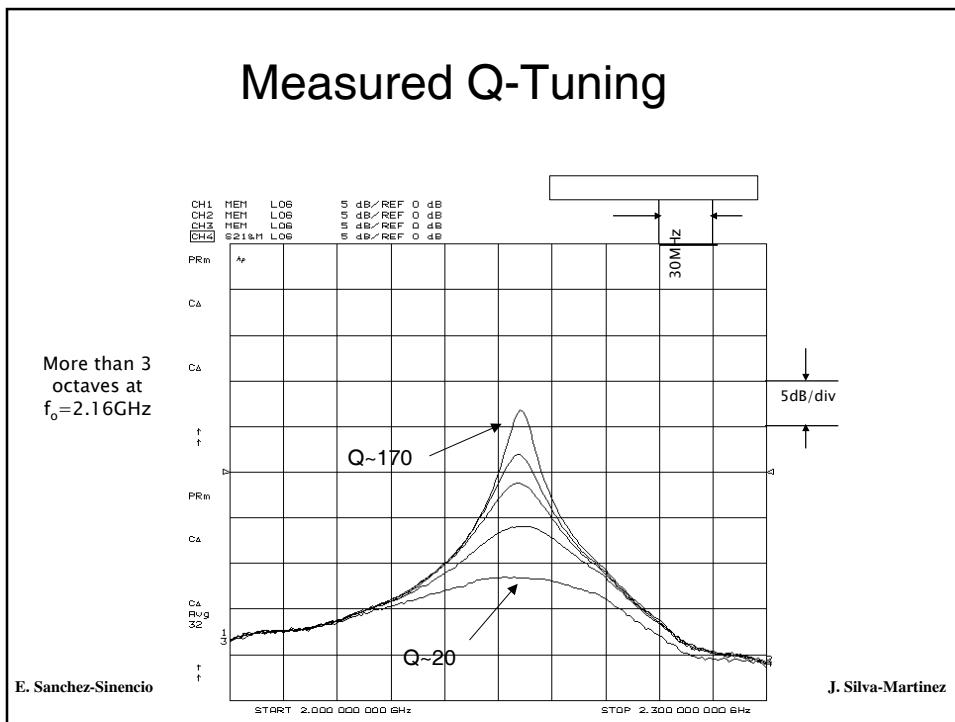
- The measurements were performed on chips enclosed in TQFP44.
- All the measurement results to be presented except the power consumption are of the entire combination of the filter, output buffer and external passive circuitry providing DC bias and impedance matching.

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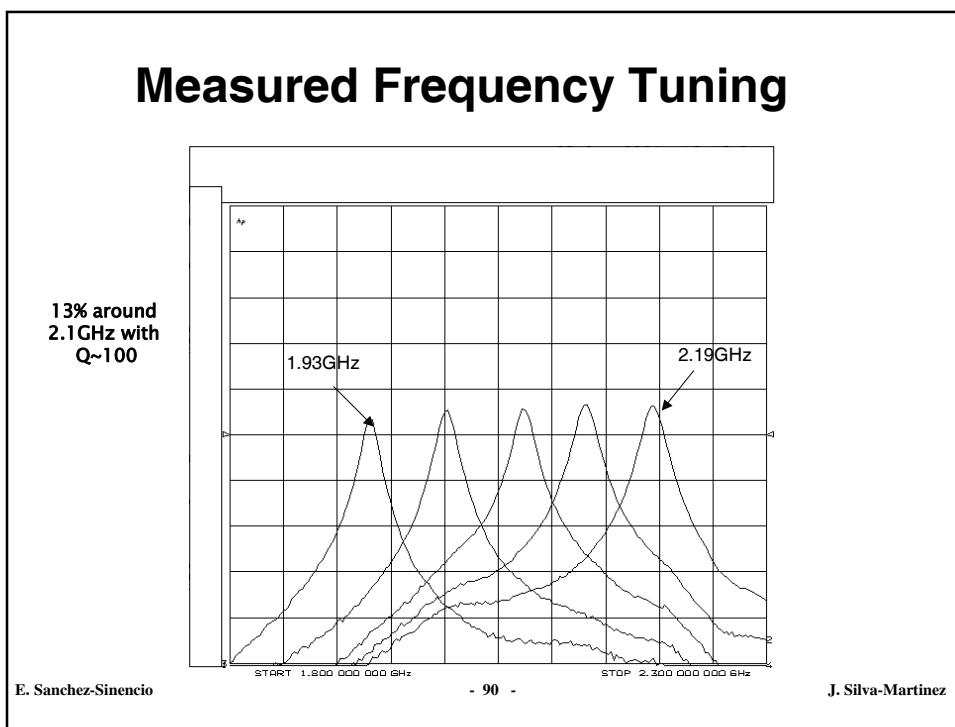
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## Measured Q-Tuning



## Measured Frequency Tuning



## Measured Peak Gain Tuning

27 Aug 2001 09:36:22

CH1	S21&M	LGS	15	dB/REF	0 dB
CH2	MEM	LGS	15	dB/REF	0 dB
CH3	MEM	LGS	15	dB/REF	0 dB
CH4	MEM	LGS	15	dB/REF	0 dB

Around  
2 octaves with  
 $f_o=2.12\text{GHz}$   
and  $Q=40$

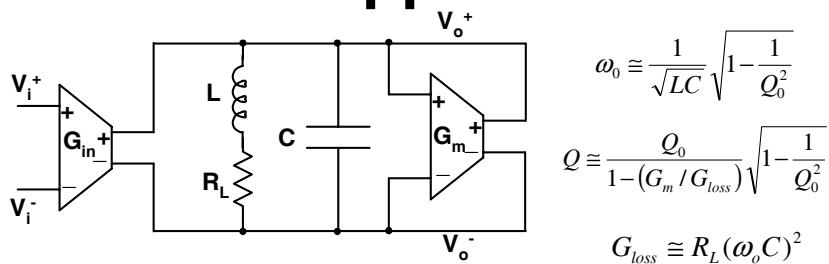
Providing gain at the  
 $\omega_o$  of an image-reject  
filter is useful in a  
receiver front-end  
after the LNA, to  
relax the NF spec of  
the mixer.

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## 2nd approach



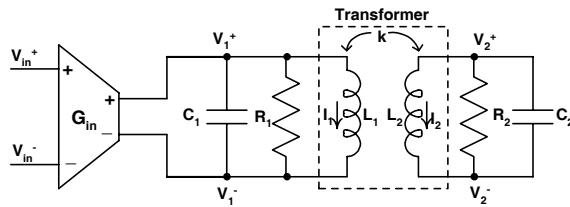
- ☺ Replace off-chip filters
- ☺ Eliminate need of impedance matching
- ☺ Reduce power, area, and cost
- ☹ Integrated spiral inductors are lossy
- ☹ Positive feedback is needed for Q-enhancement

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## Dual Resonator Bandpass Filter



$$R = R_1 = R_2 = K_2 Q \sqrt{L/C}$$

- Two magnetically coupled resonators
- 4<sup>th</sup> order filter
- $\omega_0$  is fixed by the LC product
- Coupling coefficient  $k$  is used to tune Q

$$LC = (1/\omega_0^2)$$

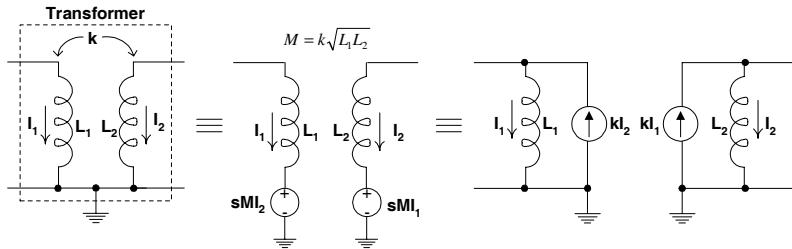
$$k = K_1 / Q$$

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## Transformer Models



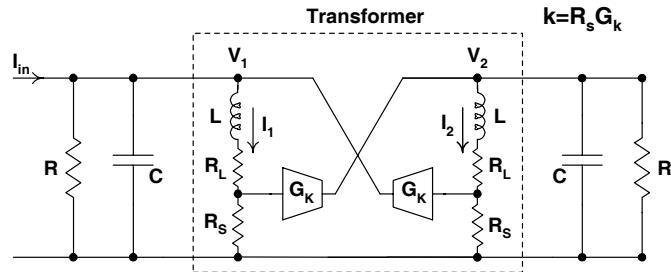
- Transformer can be replaced by induced currents
- Due to losses, induced currents are not in phase
- Severe passband ripples
- Coupling neutralization to maintain a flatband response
- Emulate the transformer action by electric coupling

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## Emulation of Magnetic Coupling



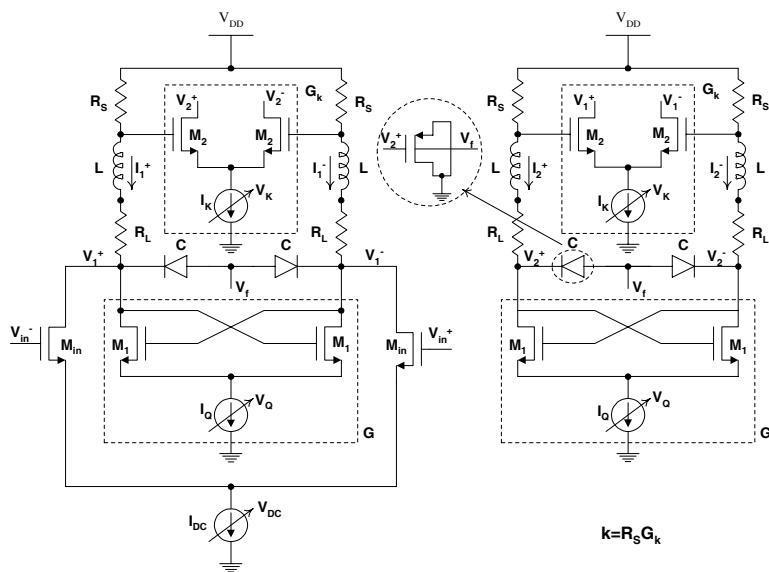
- $k = G_k R_s (\approx 0.01)$
- Provide possibility of BW tuning while maintain flatband
- Placing inductors apart to diminish magnetic coupling

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## Circuit Implementation

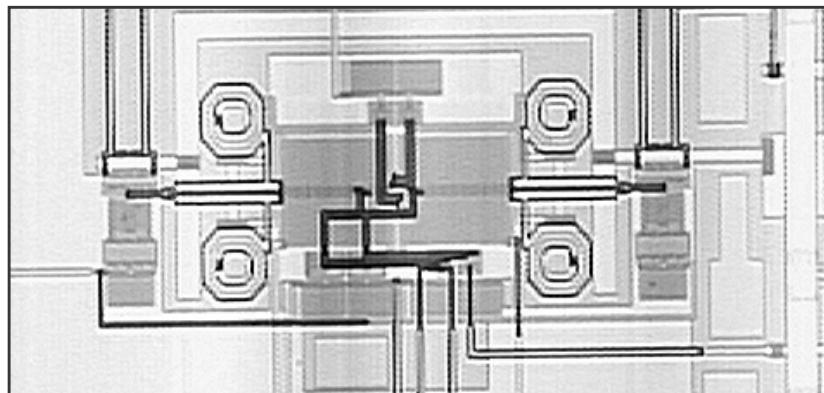


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## Chip Micrograph



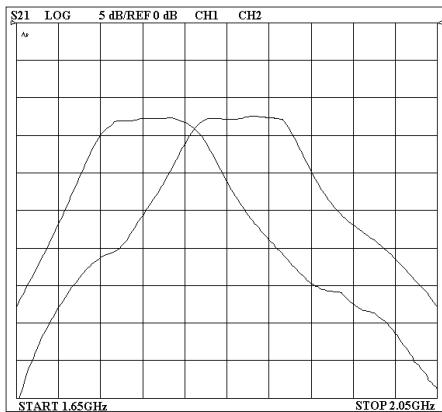
X=500 $\mu$ m, Y= 300 $\mu$ m

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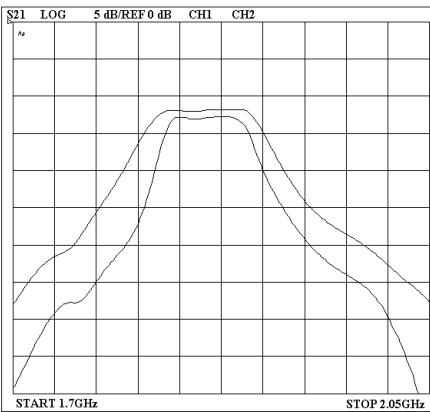
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## Measurement Results



Frequency tuning  
 $f_0=\{1.77\text{GHz}, 1.86\text{GHz}\}$



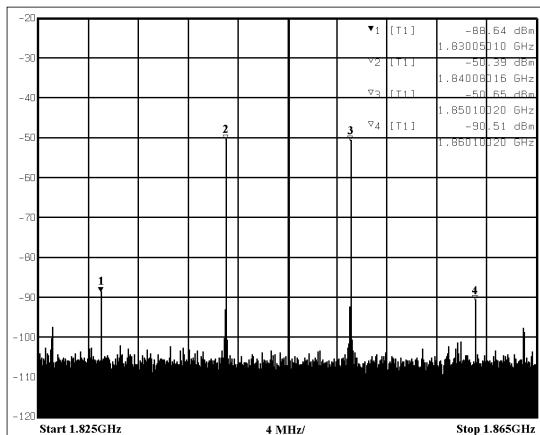
Bandwidth tuning  
BW={70MHz, 100MHz}

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## Measurement Results



Two Tone intermodulation distortion -34dBm each

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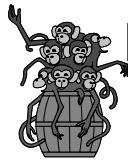
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## Frequency- and Q-tuning techniques for OTA-C filters

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## Need for Automatic Tuning

- Process variations can change  $f_o$  and Q by at least 20%
- Parameters also change with temperature and time(aging)
- Automatic tuning is a critical issue for the optimal performance of continuous-time circuits.

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## Methods of tuning

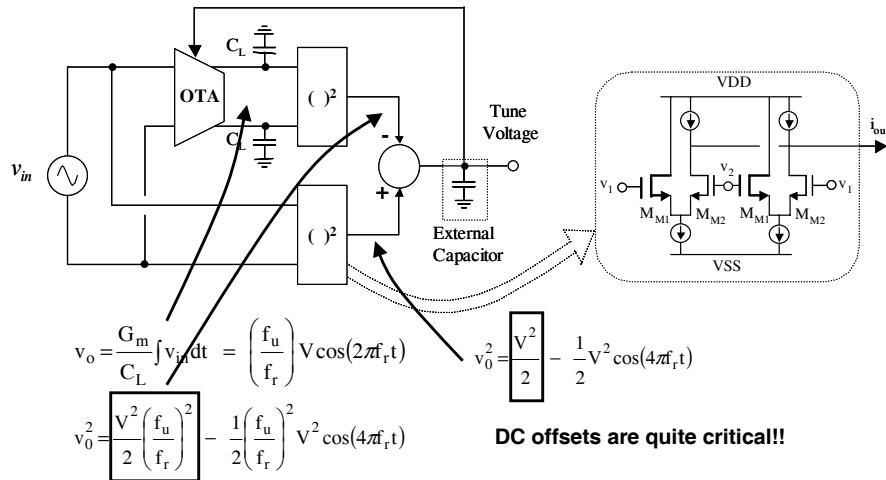
- Master-Slave
- Based on trigonometric properties
- Based on filter phase information
- Pre-tuning
- Burst tuning
- Switching techniques

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## Automatic Tuning Based on Power Comparison



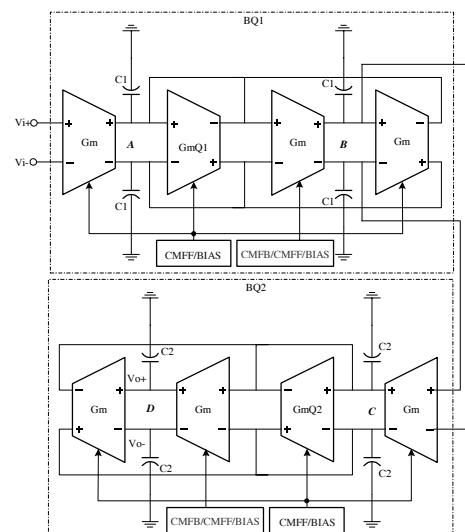
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## 4th-Order Equiripple Linear Phase Filter

- ☛ Two biquads Gm-C filter;
- ☛ C1 & C2 are total capacitance at node A,B, and C, D;
- ☛ One CM control per node;
- ☛ Node A&C--Low impedance node: CMFF/Bias;
- ☛ Node B&D-- CMFB/CMFF/Bias;

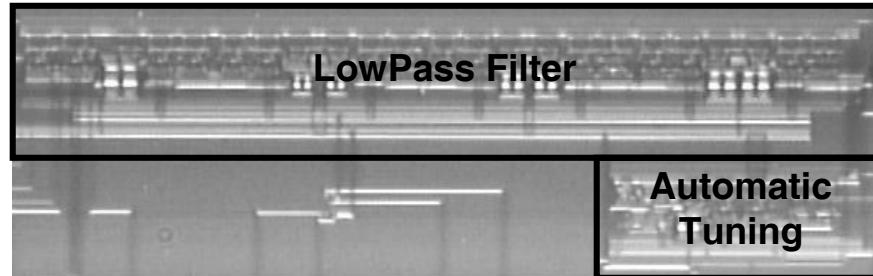


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## Microphotograph of the chip



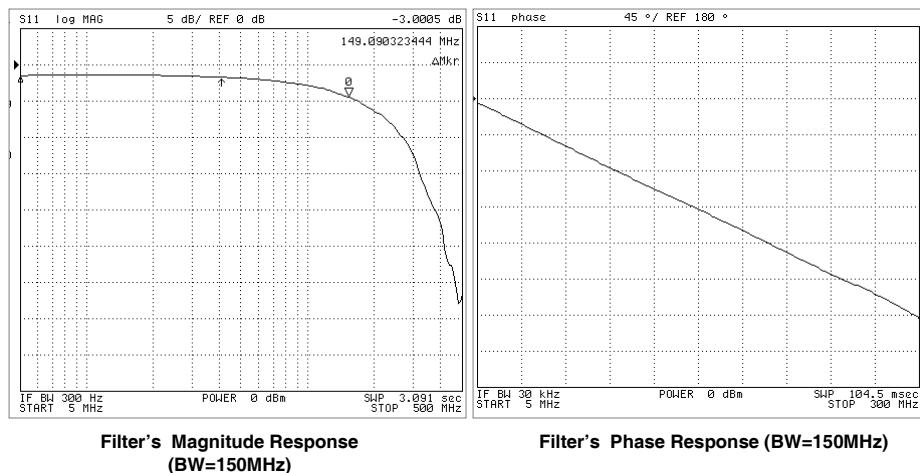
CMOS 0.35  $\mu\text{m}$  technology

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## Experimental Results: Frequency Response

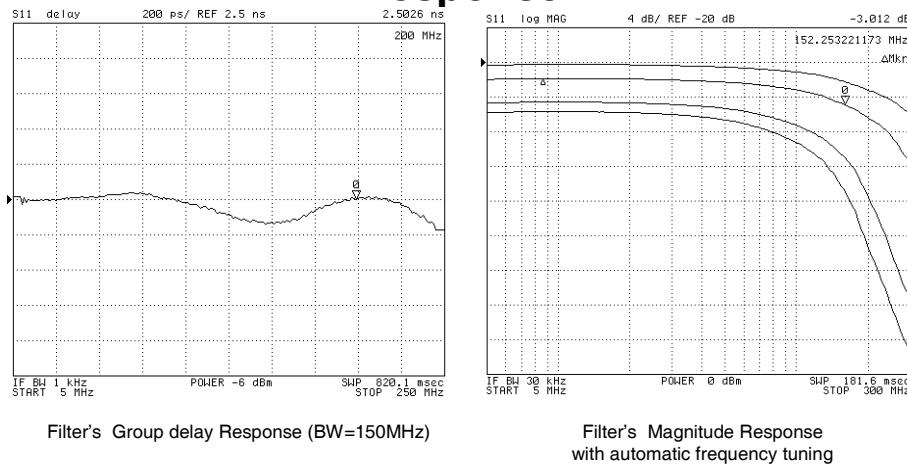


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## Experimental Results: Frequency Response

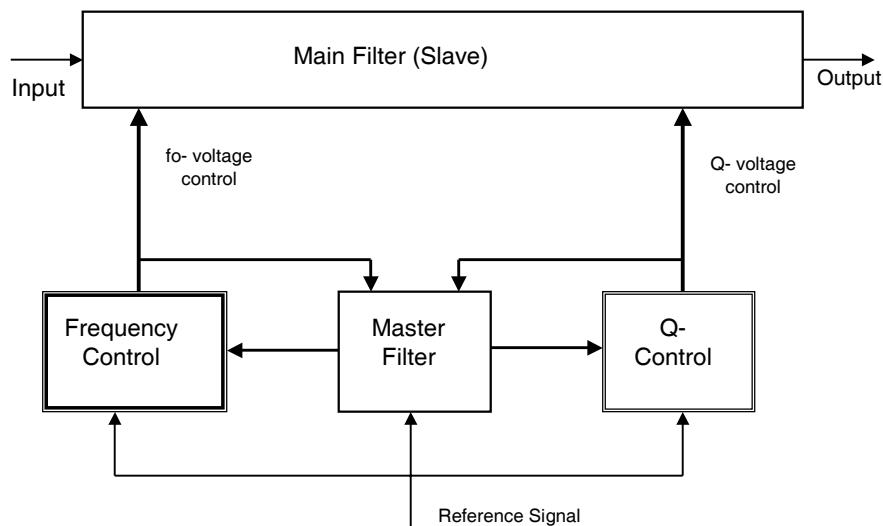


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## Master-Slave Tuning Concept



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# Frequency Tuning

## Phased-Locked Loop (PLL)

- Most widely used scheme
- Accurate (less than 1% error is reported)
- **Square wave input reference**
- Only Phase - Frequency Detector, and LPF are the additional components
- It may take a large area overhead

VCF, VCO, Single OTA, Peak detect, adaptive....

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# Q Tuning Schemes

Based on an envelope detector and a switched- capacitor integrator. It yields an accuracy of about 30%

## Modified LMS

- Q-accurate of about 1%
- It does not use envelope detector
- **Square wave input, any periodic function is sufficient**
- Independent of frequency tuning

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## Adaptive LMS Algorithm: Introduction

- Called Adaptive “Least-Mean-Squares” Algorithm because it learns by minimizing the mean-square error (MSE) between a desired response and the actual response of a system
- Minimizes error by updating system coefficients through a feedback loop

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## Adaptive LMS Algorithm: Theory

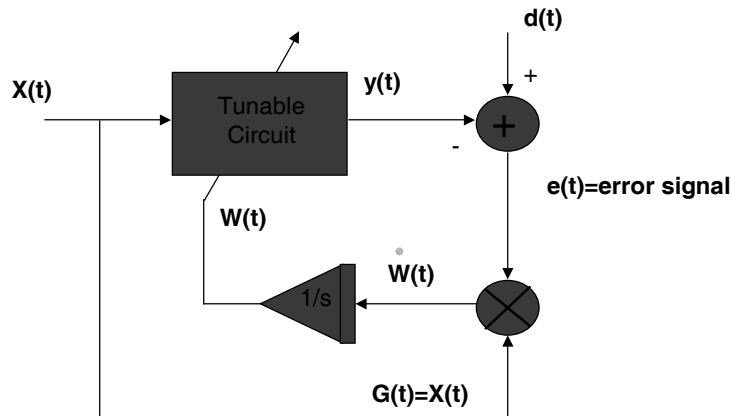
- Using the steepest descent algorithm to minimize the MSE we obtain:
- $\dot{W}(t) = k[d(t) - y(t)]G(t) = k[e(t)]G(t)$ 
  - $W(t)$  = tuning signal
  - $d(t)$  = desired system output
  - $y(t)$  = actual system output
  - $G(t)$  = tuning gradient (partial derivative of  $y(t)$  with respect to  $W(t)$ )
  - $k$  = adaptation constant

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# LMS Algorithm: Block Diagram (Linear System)

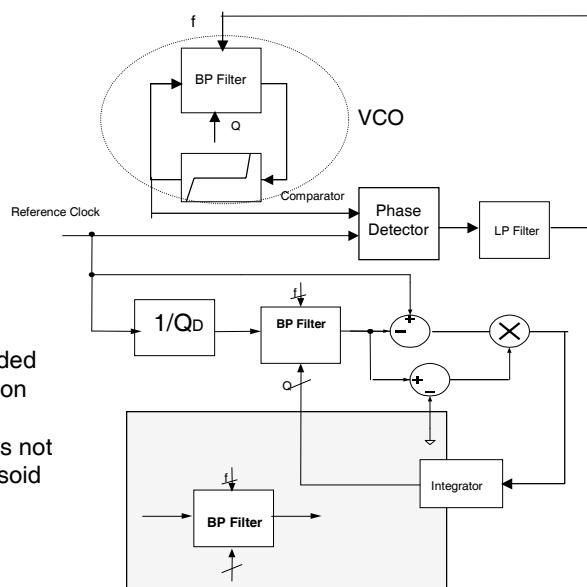


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- Three Filters are needed
- An accurate attenuation  $1/Q_D$  block is required
- Reference signal does not need to be a pure sinusoid



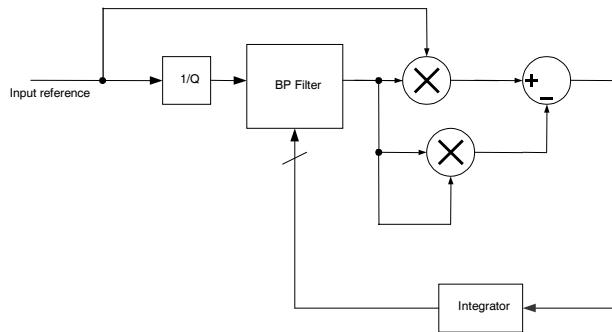
Stevenson, J.M.; Sanchez-Sinencio, E "An accurate quality factor tuning scheme for IF and high-Q continuous-time filters". IEEE Journal of Solid-State Circuits, Volume: 33 No.12 , Dec. 1998 , Page(s): 1970 -1978

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## An enhanced Q-tuning scheme



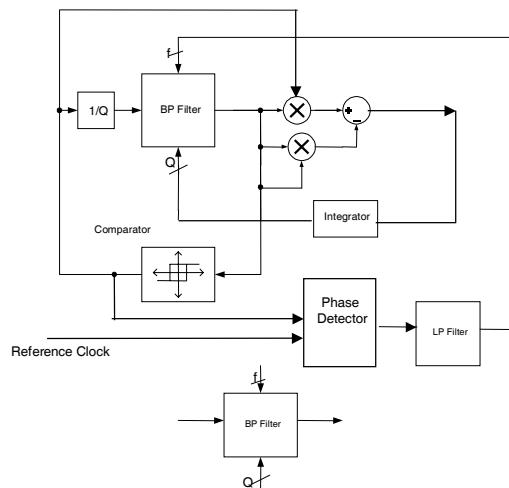
New implementation of modified-LMS Q-tuning scheme. Note that the LMS has been implemented in a different way yielding a structure with less offset voltages. See reference for more details.

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## The enhanced tuning scheme



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# Improvements over the previous Tuning scheme comparison

- Area overhead decreased

(Previous scheme => 2 extra filters

New scheme => 1 extra filter )



- Eases the matching restrictions

(Previous tuning scheme => match 3 filters

New tuning scheme => match 2 filters )

- Improves accuracy of tuning

(New tuning scheme is more tolerant to offsets than the previous one)

The Q-tuning loop speed can be equal to the fo-tuning loop for optimal

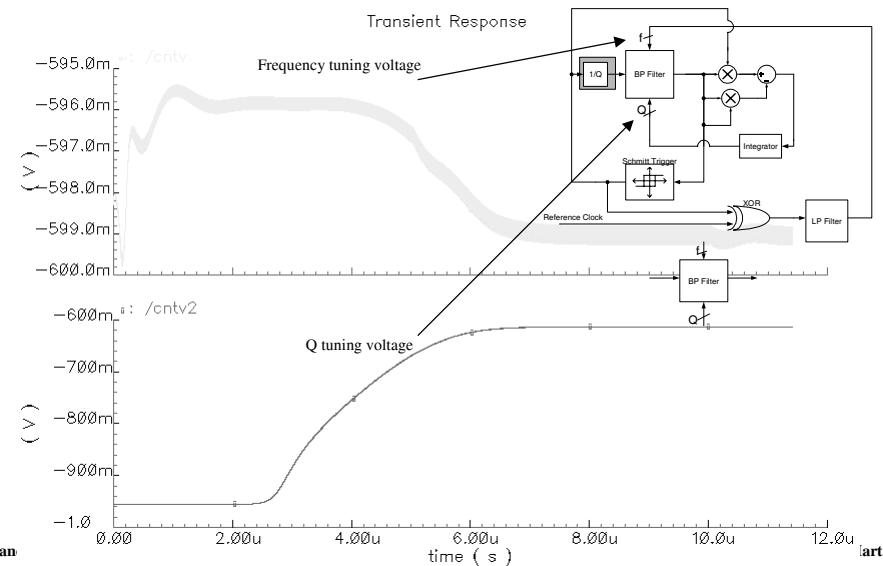
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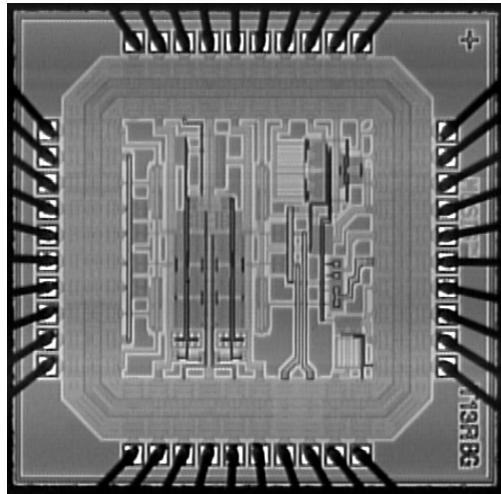
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## Simulated results for tuning

proj\_2 fQ\_tun1 schematic : Dec 4 23:51:02 2000



## Die Photograph

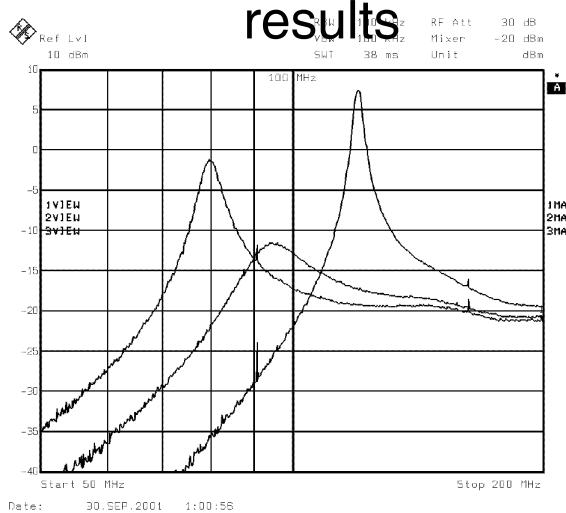


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## Experimental Q tuning range results

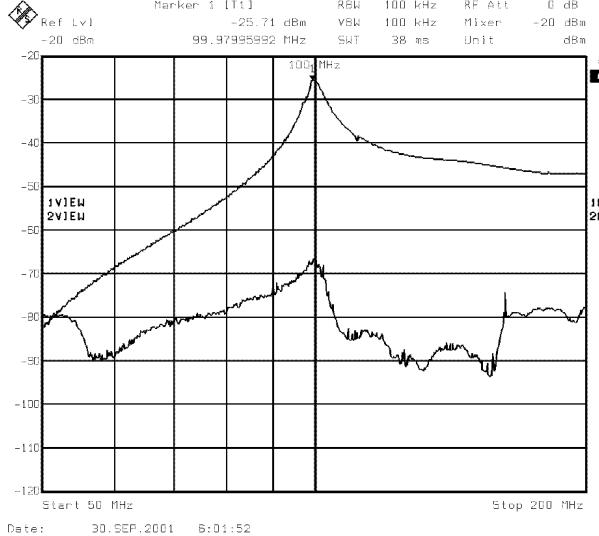


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Qs of 16, 5 and 40 at 80,95 and 110 MHz

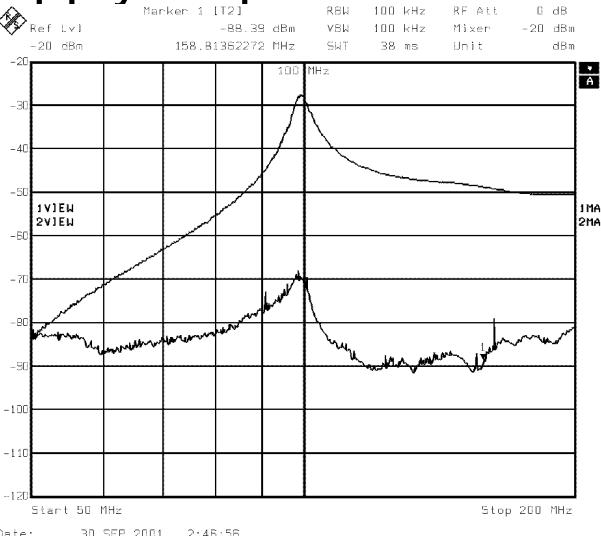
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## DM-CM response of the filter



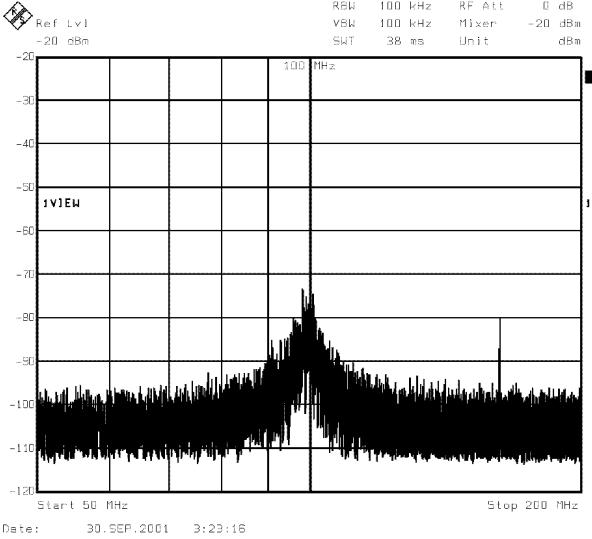
- CMRR is more than 40dB in the band of interest

## Supply response of the filter



- PSRR is more than 40dB in the band of interest

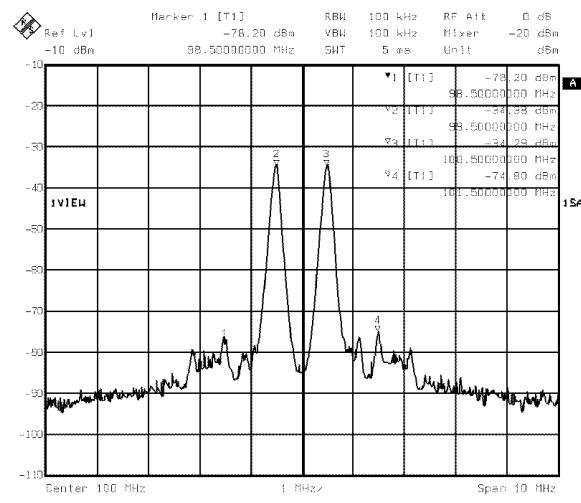
## Noise response of the filter



E. Sanchez-Sinencio Total integrated noise power at the output= -60dBm

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## Two-tone inter-modulation test



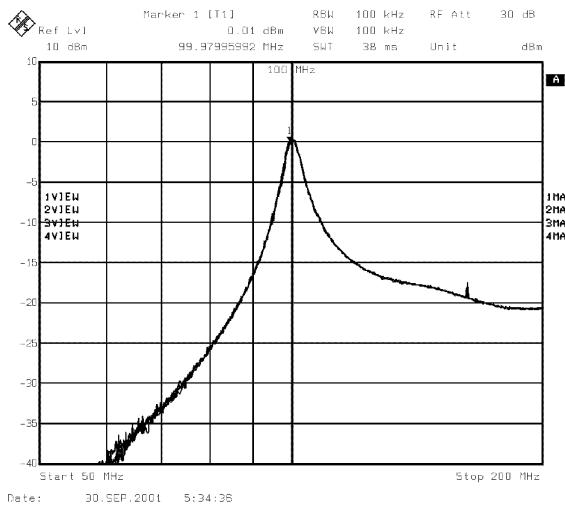
IM<sub>3</sub> of < -40dB when the input signal is 44.6mV

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## Filter response for four different ICs



E. Sanchez-Sinencio • The tuning works!

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## Open problems

- ☞ Efficient solutions with  $\text{SNDR} > 70 \text{ dB}$  for  $1\text{Vpk-pk}$  are needed for wireline applications
  - ⇒ frequency range 10 MHz - 50 MHz
  - ⇒ Noise density  $< -148 \text{ dBm}$
  - ⇒ IM3  $\sim -65 \text{ dB}$  A 1 Vpk-pk
  - ⇒ Accuracy  $\sim 1\text{-}2 \%$
- ☞ Solutions for next generation of read channels
  - ⇒ Frequency range 200 MHz - 800 MHz
  - ⇒ Gain boosting  $\sim 20 \text{ dB}$
  - ⇒ Accuracy  $\sim 5 \%$
  - ⇒ Very low power and small area
- ☞ RF Filters
  - ⇒ Small noise figure
  - ⇒ High-Q solutions
  - ⇒ Precision better than 1 % (efficient automatic tuning circuitry)

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# Conclusions



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**See also [JSSC](#), [TCAS](#), [ISSCC](#), [ISCAS](#), [CICC 2001-2004](#)**